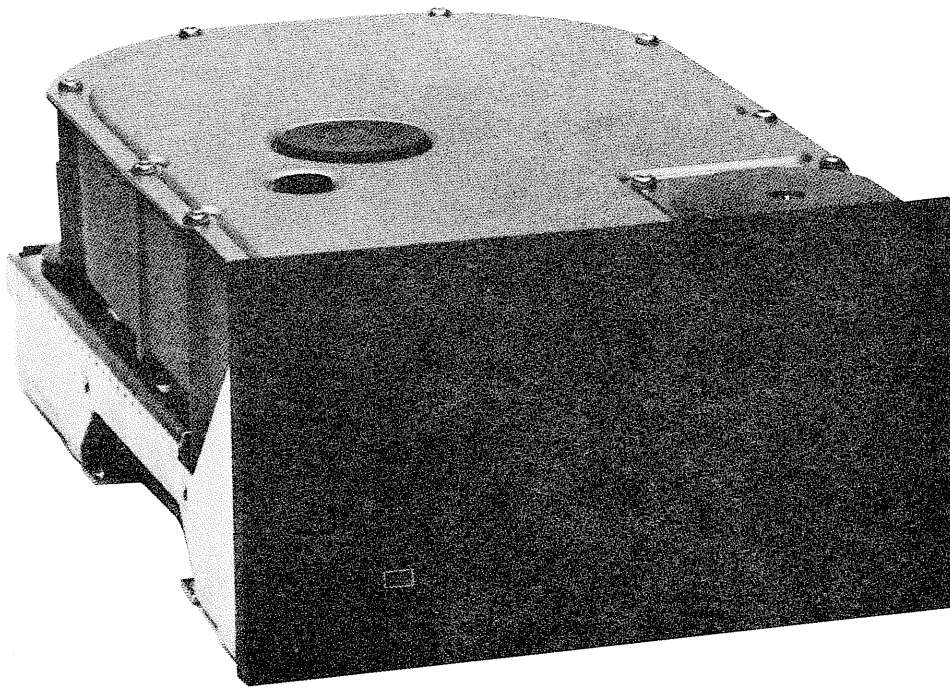


OEM  
OPERATING AND SERVICE MANUAL  
TM501, TM502, TM503 DISK DRIVES  
345 TRACKS PER INCH



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# SECTION 1

## GENERAL DESCRIPTION

### INTRODUCTION

This manual provides useful information to assist the customer when incorporating the Tandon rigid disk drive into a system.

Tandon Corporation's TM500 series of drives are full feature, 5-1/4-inch, rigid disk drives. They are compact data storage devices that contain one or more 130-millimeter plated aluminum platters within a sealed housing.

The TM500 series includes Model Numbers TM501, TM502, and TM503, which have one, two, and three recording platters, and use two, four, and six recording heads, respectively.

#### 1.1 SCOPE OF THE DOCUMENT

Section 1 of this manual contains a general description of the disk drives. Section 2 contains the product specifications. Section 3 provides information on operation of the drives. Section 4 is a troubleshooting guide and replacement procedure. Assemblies and schematics are included in the appendices.

#### 1.2 PURPOSE OF THE DRIVE

The 5-1/4-inch disk drive is a rotating disk memory device designed for random access data storage and retrieval. Typical applications include word processing systems, entry level microprocessor systems, intelligent calculators, program storage, small business computer systems, and any application in which low cost, random access data storage is required.

### 1.3 MAJOR FEATURES

#### MICROPROCESSOR CONTROL

The TM500 series of drives feature an onboard microprocessor. The microprocessor provides five major functions:

1. Self-calibration on power-up.
2. Buffered seek timing for improved access times.
3. Improved positioning with reduced hysteresis.
4. Write current switching for optimal recording quality.
5. Power and track fault detection.

#### DAISY CHAIN CAPABILITY

The drive provides the address selection and gating functions necessary to daisy chain a maximum of four units at the user's option. The last drive on the daisy chain terminates the interface. The terminations are accomplished by a resistor array plugged into a DIP socket.

#### INDUSTRY STANDARD INTERFACE COMPATIBILITY

The drive is compatible with controllers that use an industry standard interface.

## ACTIVITY INDICATOR

The activity indicator is located on the front panel of the drive. It is automatically illuminated when the drive is selected.

## AIR FILTRATION

A self-contained, recirculating air filtration system supplies clean air through a 0.3-micron filter. A secondary absolute filter is provided to allow pressure equalization with the ambient atmosphere without contamination. The entire head-disk-actuator compartment is maintained at a slightly positive pressure to further ensure an ultraclean environment.

## 1.4 FUNCTIONAL DESCRIPTION

The drive is fully self-contained and requires no operator intervention during normal operation. During the power-up sequence, the spindle motor reaches 3600 RPM, and the positioning mechanism recalibrates the recording heads back to Track 0. At this time, a Ready signal on the interface indicates the drive is ready for operation.

The head is positioned over the desired track by means of a four-phase stepper motor/band assembly and its associated electronics. This positioner uses a one-step rotation to cause a one-track radial movement. Subsequently, the recording heads can be positioned over the desired cylinders, and the data can be read or written from the appropriate track by selecting the desired head.

Typically, the drive uses MFM write and read recording methods. Data recovery electronics include a low-level read amplifier, differentiator, a zero-crossover detector, and digitizing circuits. No data decoding feature is provided on the drives.

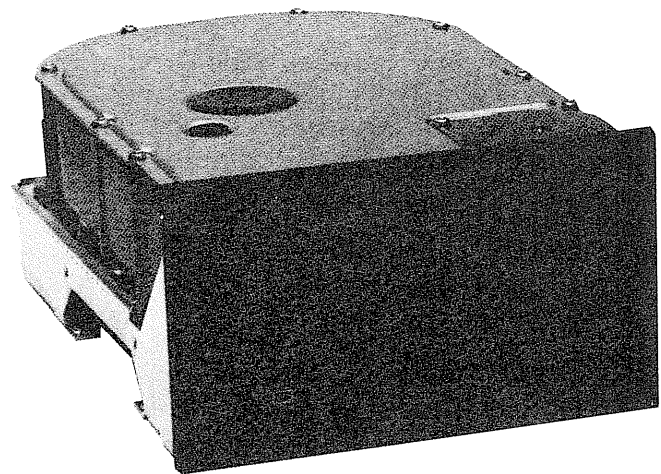
The drive has the following sensor systems:

1. An optical Track 0 switch senses when the Head/Carriage Assembly is positioned at Track 0.

2. An index sensor, which consists of a magnetic pick-up and index hole positioned to provide an analog signal when an index hole is detected.

## 1.5 PHYSICAL DESCRIPTION

The TM500 drive is shown in Figure 1-1. The drives contain 130 millimeter storage media that rotate at 3600 RPM, using a direct drive, brushless D. C. motor. The recording is accomplished by noncontact standard recording heads that are moved by a precision split band positioning device and stepper motor.



**FIGURE 1-1  
DISK DRIVE**

The Head Disk Assembly is enclosed in a sealed cast aluminum housing, which includes an air filtration system to ensure a contamination-free environment. The housing is shock mounted to a metal frame that has the front panel attached, and threaded holes on the sides and bottom for mounting the drive onto a chassis.

In addition, the drive includes the read/write control electronics, the servo spindle control electronics, an index sensor, a brake assembly, and a front panel indicator.



# **SECTION 2**

## **PRODUCT SPECIFICATIONS**

### **INTRODUCTION**

This section contains the mechanical, electrical and operational, reliability, and environmental specifications for the TM501, TM502, and TM503 disk drives.

#### **2.1 MECHANICAL SPECIFICATIONS**

The mechanical and physical dimensions are contained in Figure 2-1.

#### **2.2 ELECTRICAL AND OPERATIONAL SPECIFICATIONS**

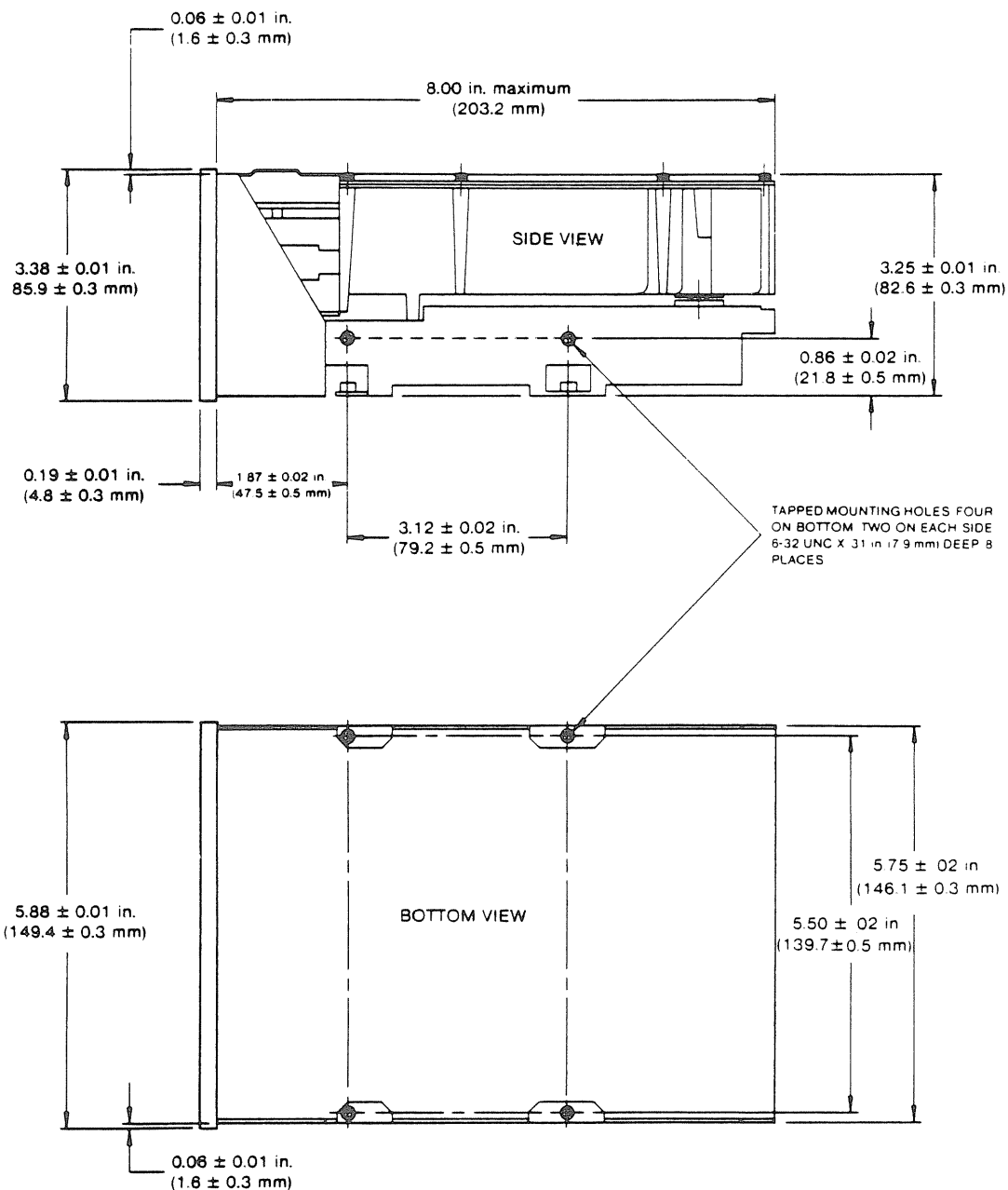
The electrical and operational specifications are contained in Table 2-1. Typical starting current requirements at nominal voltage are contained in Figure 2-2.

#### **2.3 RELIABILITY SPECIFICATIONS**

The reliability specifications are contained in Table 2-2.

#### **2.4 ENVIRONMENTAL SPECIFICATIONS**

The environmental specifications are contained in Table 2-3.



Note: Weight is 3.0 kilograms, 6.5 pounds maximum

**FIGURE 2-1**  
**DISK DRIVE OUTLINE DRAWING**

**TABLE 2-1**  
**ELECTRICAL AND OPERATIONAL SPECIFICATIONS**

|  |  |
|--|--|
| Media  | Lubricated, 130 millimeter, plated aluminum disk |
| Tracks Per Inch  | 345 TPI  |
| Spacing, Track to Track  | 2.9 milinches                                    |
| Number of Cylinders  | 306 cylinders                                    |
| Number of Tracks   |  |
| TM501  | 612 tracks                                       |
| TM502  | 1224 tracks                                      |
| TM503  | 1836 tracks                                      |
| Disk Speed   | 3600 RPM $\pm$ 1 percent                         |
| Average Latency  | 8.33 milliseconds                                |
| Start Time   | 15 seconds maximum                               |
| Stop Time  | 15 seconds maximum                               |
| Seek Time  | 3 milliseconds track to track                    |
| Head Settling Time   | 15 milliseconds, last track accessed             |
| Average Access Time,<br>Including Head Settling Time,<br>3 Millisecond Step Rate | 321 milliseconds                                 |
| Average Access Time<br>Using Buffered Seek,<br>Including Head Settling Time      | 85 milliseconds                                  |
| Transfer Rate  | 5 megabits per second                            |

**TABLE 2-1 (CONTINUED)**  
**ELECTRICAL AND OPERATIONAL SPECIFICATIONS**

Maximum Flux Reversal Density

9090 FRPI

Unformatted Capacity Per Drive

TM501

6.38 megabytes

TM502

12.76 megabytes

TM503

19.14 megabytes

Unformatted Capacity Per Surface

3.19 megabytes

Unformatted Capacity Per Track

10.4 kilobytes

#### **POWER REQUIREMENTS**

+ 12 volts D. C.  $\pm 10$  percent, 1.5 amperes typical, 5 amperes maximum during motor start-up, not to exceed 12 seconds, 2 amperes maximum running, with no more than 50 millivolts Periodic and Random Deviation (PARD).

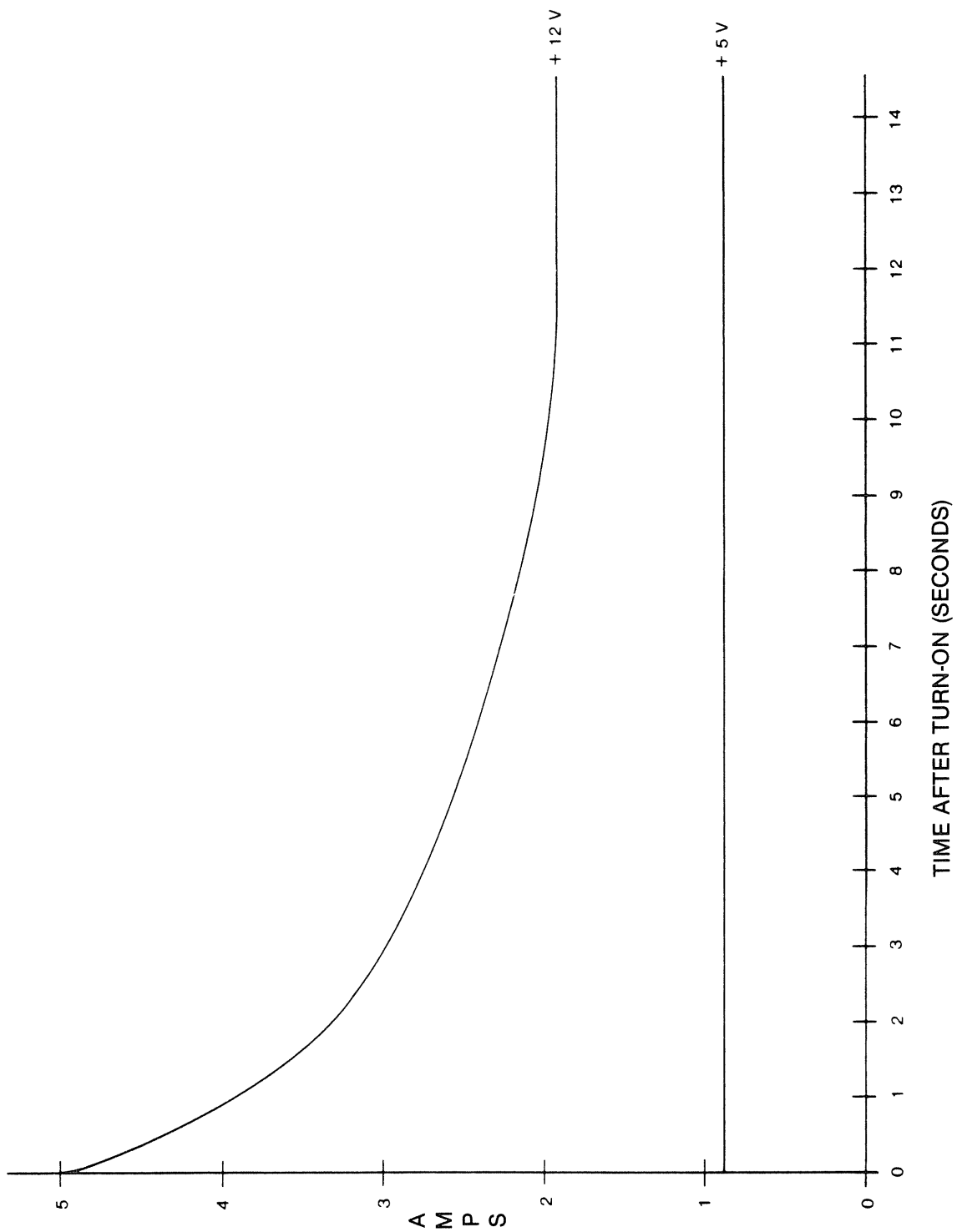
+ 5 volts D. C.  $\pm 5$  percent, 0.8 amperes typical, 1.2 amperes maximum running, with no more than 50 millivolts PARD.

There are no restrictions in sequencing power supplies on or off.

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**179045-001**  
**REV. A**



**FIGURE 2-2**  
**TYPICAL STARTING CURRENTS**

**TABLE 2-2**  
**RELIABILITY SPECIFICATIONS**

**SOFT AND HARD READ ERROR RATES, EXCLUSIVE OF MEDIA DEFECTS**

For data that has been verified previously as error free, and when used in conjunction with a data separator and phase lock loop of good design, the recoverable (soft) read error rate for any subsequent read operation shall not exceed one error in  $1 \times 10^{10}$  bits transferred. A recoverable read error is an error that may be corrected within five attempts to reread the data.

The nonrecoverable (hard) read error rates shall not exceed one error in  $1 \times 10^{12}$  bits transferred. A nonrecoverable read error is an error that may not be corrected within five attempts to reread data, providing that the writing of the data previously has been verified as correct. The seek error rate is not to exceed one error in  $1 \times 10^6$  seeks.

**MEDIA DEFECTS**

Any defects on the media surface will be identified on a defect map provided with each drive. This defect map will indicate the head number, track number, and number of bytes from index for each defect. Each defect shall be no longer than 16 bits. Cylinders 000 and 001 are guaranteed error free.

The map is offered as a guide only. The number of defects and their location can change due to customer system variations such as data separators.

|                            |                       |
|----------------------------|-----------------------|
| Mean Time Between Failures | 11,000 power on hours |
| Mean Time To Repair        | 30 minutes            |
| Component Design Life      | 5 years               |
| Preventative Maintenance   | Not required          |

**TABLE 2-3  
ENVIRONMENTAL SPECIFICATIONS**

|  |  |
|--|--|
| <p>Ambient Temperature</p> <p>    Operating</p> <p>    Nonoperating</p> <p>Temperature Gradient</p> <p>    Operating</p> <p>    Nonoperating</p> <p>Relative Humidity</p> <p>Relative Humidity Gradient</p> <p>    Operating</p> <p>    Nonoperating</p> <p>Maximum Wet Bulb Temperature</p> <p>Elevation</p> <p>    Operating</p> <p>    Nonoperating</p> | <p>4°C to 50°C, 39°F to 122°F</p> <p>-40°C to 60°C, -40°F to 140°F</p> <p>10°C per hour, 18°F per hour</p> <p>Below that causing condensation</p> <p>8-to-80 percent, noncondensing</p> <p>20 percent per hour</p> <p>Below that causing condensation</p> <p>26°C, 78.8°F, without condensation</p> <p>Density Altitude: -457 to 2,972 meters,<br/>-1,500 to 9,750 feet</p> <p>Sea level to 3,650 meters,<br/>Sea level to 12,000 feet</p> |
| <p><b>Tandon</b> CORPORATION • CHATSWORTH, CALIFORNIA 91311</p>  | <p align="center"><b>179045-001<br/>REV. A</b></p>   |





# SECTION 3

## OPERATION

### INTRODUCTION

This section contains information pertinent to the handling, inspection, installation, and operation of the TM500 series of drives.

#### 3.1 UNPACKING THE DRIVE

Each drive is shipped in a protective container which, when bulk packaged, minimizes the possibility of damage during shipment.

Visually examine the shipping container for possible damage. Notify the carrier immediately if any damage is found.

The following procedure is recommended for unpacking the drive.

1. Place the shipping container on a flat work surface.
2. Cut the tape on the shipping container.
3. Remove the foam lid and pads from the shipping container.
4. Remove the inner container.
5. Remove the drive from the inner container.
6. Place the drive on a foam lined surface.

#### CAUTION

*Do not manually rotate the stepper motor or spindle motor. Damage to the heads and disk may result.*

#### NOTE

The inside chamber of the drive is a sealed compartment that must not be opened.

When returning the drive to the service center, be

sure to use prior steps in reverse order, and ensure the foam stiffeners in the proper location, with the cardboard dividers properly in place between the drives (see Figure 3-1).

#### 3.2 PREINSTALLATION CHECKOUT

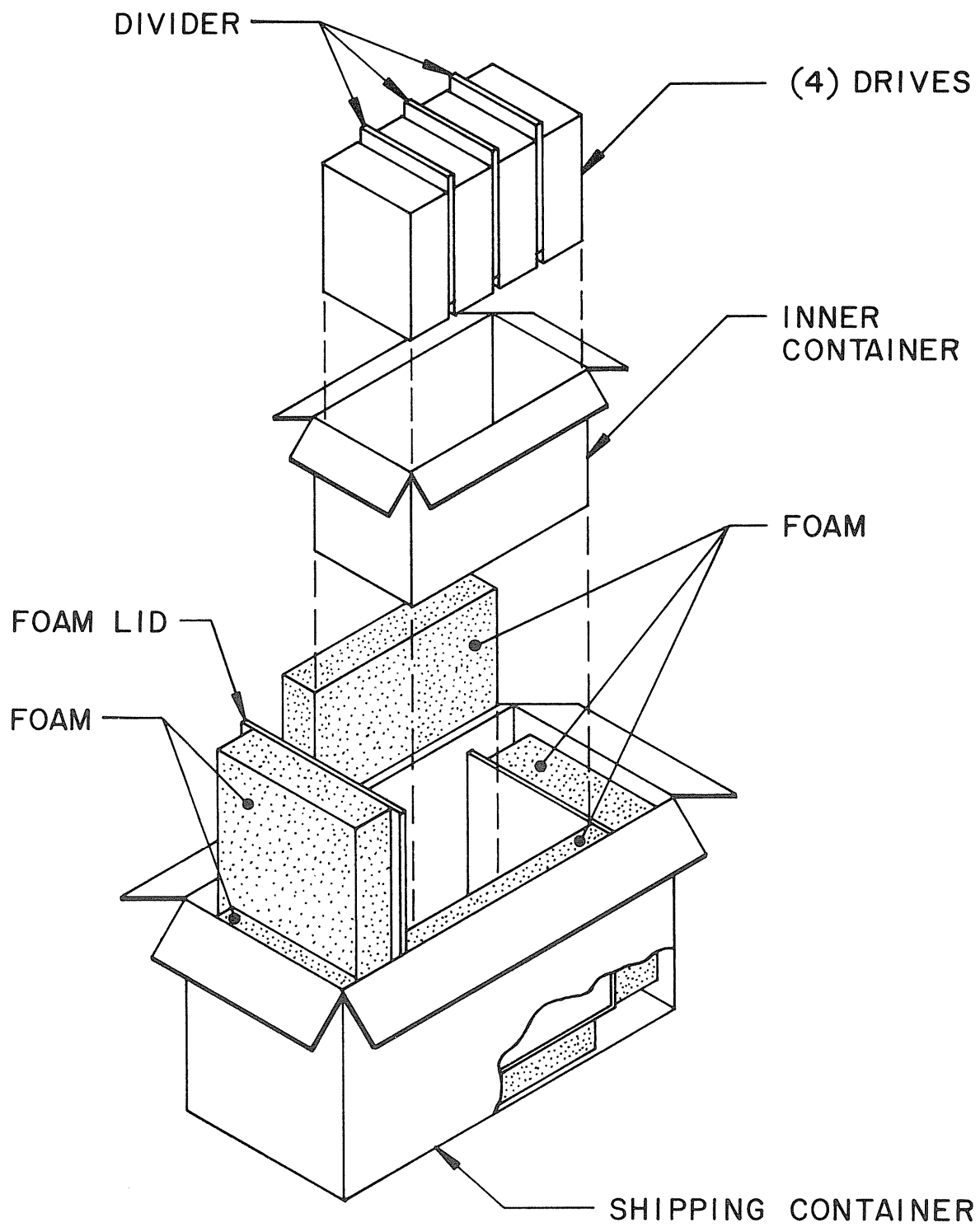
Before applying power to the drive, inspect for the following:

1. Ensure the front panel is secure.
2. Ensure the circuit board is secure.
3. Ensure the connectors are firmly seated.
4. Ensure there is no debris or foreign material between the frame and the head/disk casting.
5. Ensure the head/disk housing can move freely on the shock mounts of the frame.
6. Ensure the termination resistor pack and jumper blocks are firmly seated and in the correct configuration.

#### 3.3 MOUNTING THE DRIVE

The drive can be mounted in any vertical or horizontal plane. Eight 6-32 tapped holes are provided for mounting: two on each side and four on the bottom of the frame (see Figure 2-1, page 2-2). The drive is manufactured with some critical internal alignments that must be maintained. Hence, it is important the mounting hardware does not introduce significant stress on the drive.

Any mounting scheme in which the drive is part of the structural integrity of the enclosure is not permitted. Mounting schemes should allow for adjustable brackets or incorporate resilient members to accommodate tolerances.



**FIGURE 3-1**  
**4-PACK SHIPPING CONTAINER**

## DUST COVER

The design of an enclosure should incorporate a means to prevent contamination from loose items, e.g., dust, lint, and paper chad since the drive does not have a dust cover.

## FREE AIR FLOW

When the drive is mounted so the components have access to the free flow of air, normal convection cooling allows operation over the specified temperature range (see Table 2-3, page 2-7).

## CONFINED ENVIRONMENT

When the drive is mounted in a confined environment, air flow must be provided to maintain specified air temperatures in the vicinity of the motors and the circuit boards.

### 3.4 INTERFACE CONNECTORS

The electrical interface between the drive and the host system is via three connectors. J1 provides control signals for the drive (see Figure 3-2). J2 provides for the radial connection of read/write

data signals (see Figure 3-3). J3 provides for D.C. power (see Figure 3-4).

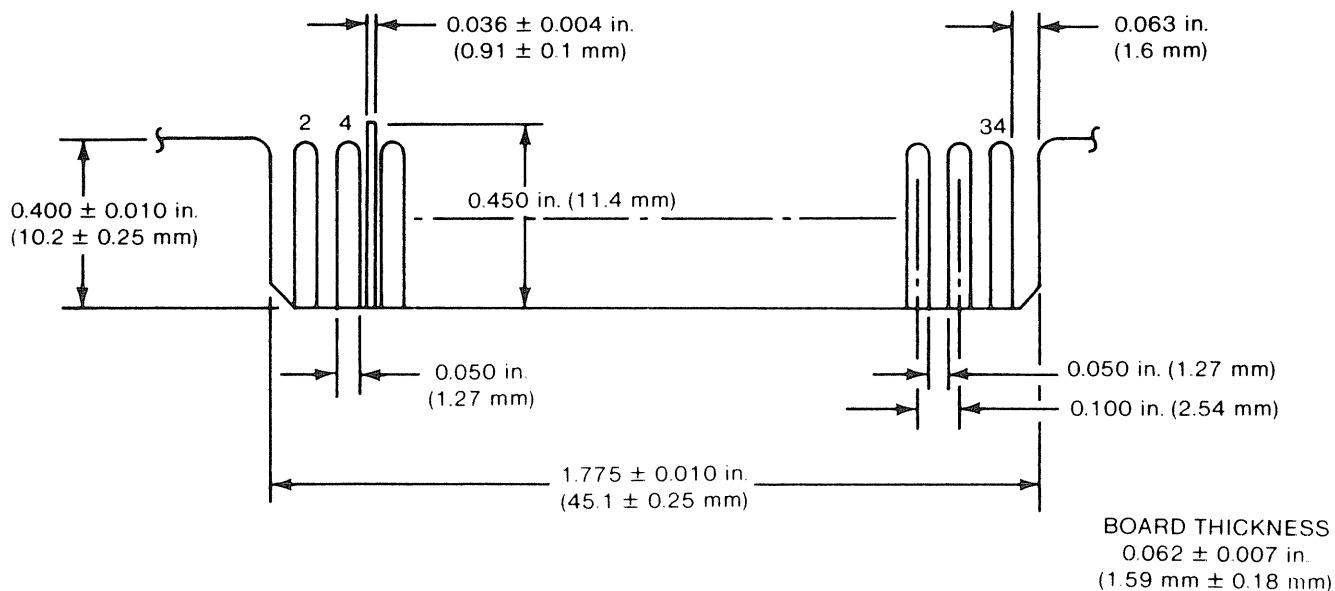
Table 3-1 contains interface lines. The interface description of the connectors, and the location of each, is contained in this section.

#### J1/P1 CONNECTOR

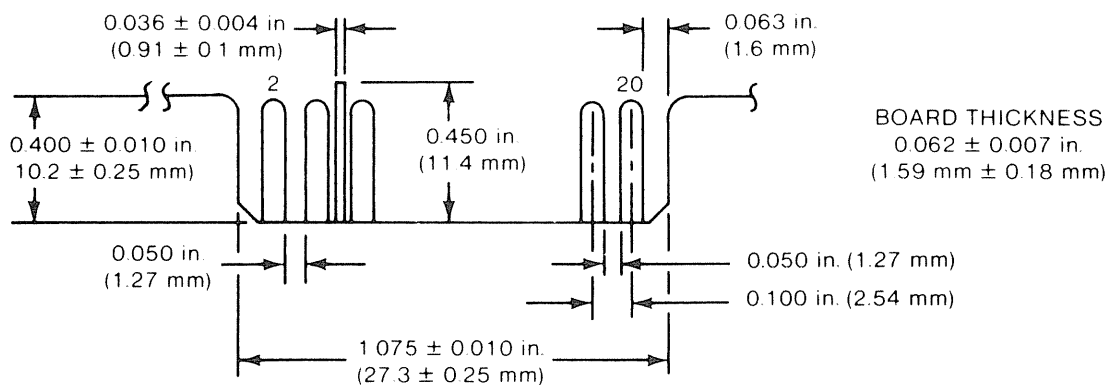
Connection to J1 is through a thirty-four-pin circuit board connector. Figure 3-2 contains the dimensions of this connector. The pins are numbered 1 through 34. The even pins are located on the component side of the circuit board. Pin 2 is located on the end of the circuit board connector closest to the D. C. power connector J3/P3, and is labeled. A key slot is provided between Pins 4 and 6. The recommended mating connector for P1 is 3M ribbon connector P/N 3463-0001, without ears.

#### J2/P2 CONNECTOR

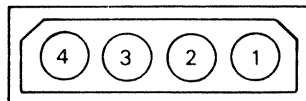
Connection to J2 is through a 20-pin circuit board edge connector. Figure 3-3 contains the dimensions of this connector. The pins are numbered 1 through 20. The even pins are located on the component side of the circuit board. The recommended mating connector for P2 is 3M ribbon connector P/N 3461-0001, without ears. A key slot is provided between Pins 4 and 6.



**FIGURE 3-2**  
**J1 EDGE CONNECTOR DIMENSIONS**



**FIGURE 3-3**  
**J2 EDGE CONNECTOR DIMENSIONS**



**FIGURE 3-4**  
**J3 POWER CONNECTOR**

**TABLE 3-1  
DRIVE INTERFACE SIGNALS AND PIN ASSIGNMENTS**

| Interface<br>Pin Number                                  |        |        |             |     |                            |
|--|--------|--------|-------------|-----|----------------------------|
| Connector  | Signal | Ground | Signal Type | I/O | Name of Signal             |
| P1<br>↑<br>34-Pin<br>Ribbon<br>Daisy<br>Chain<br>↓<br>P1 | 2      | (1)    | S           | I   | Spare                      |
|  | 4      | (3)    | S           | I   | Head Select 2 <sup>2</sup> |
|  | 6      | (5)    | S           | I   | Write Gate                 |
|  | 8      | (7)    | S           | O   | Seek Complete              |
|  | 10     | (9)    | S           | O   | Track 0                    |
|  | 12     | (11)   | S           | O   | Fault                      |
|  | 14     | (13)   | S           | I   | Head Select 2 <sup>0</sup> |
|  | 16     | (15)   | —           | —   | Reserved (To J2 - 7)       |
|  | 18     | (17)   | S           | I   | Head Select 2 <sup>1</sup> |
|  | 20     | (19)   | S           | O   | Index                      |
|  | 22     | (21)   | S           | O   | Ready                      |
|  | 24     | (23)   | S           | I   | Step                       |
|  | 26     | (25)   | S           | I   | Drive Select 0             |
|  | 28     | (27)   | S           | I   | Drive Select 1             |
|  | 30     | (29)   | S           | I   | Drive Select 2             |
|  | 32     | (31)   | S           | I   | Drive Select 3             |
|  | 34     | (33)   | S           | I   | Direction In               |
| P2<br>↑<br>20-Pin<br>Ribbon<br>Radial<br>↓<br>P2         | 1      | (2)    | S           | O   | Drive Select               |
|  | 3      | (4)    | S           | —   | Spare                      |
|  | 5      | (6)    | —           | —   | Reserved                   |
|  | 7      | (8)    | —           | —   | Reserved (To J1-16)        |
|  | 9      | (10)   | —           | —   | Spare                      |
|  | 11     | (12)   | —           | —   | Ground                     |
|  | 13     | —      | D           | I   | + Write Data               |
|  | 14     | —      | D           | I   | - Write Data               |
|  | 15     | (16)   | —           | —   | Ground                     |
|  | 17     | —      | D           | O   | + Read Data                |
|  | 18     | —      | D           | O   | - Read Data                |
|  | 19     | (20)   | —           | —   | Ground                     |
| P3<br>↑<br>Radial<br>P3                                  | 1      |        |             | —   | + 12 volts D. C. In        |
|  | 2      |        |             | —   | + 12 volts D. C. Return    |
|  | 3      |        |             | —   | + 5 volts D. C. Return     |
|  | 4      |        |             | —   | + 5 volts D. C. In         |

**NOTES:**

1. S = Single Ended
2. D = Differential
3. I = Drive Input
4. O = Drive Output

## J3/P3 CONNECTOR

D. C. power connector J3 is a four-pin AMP Mate-N-Lok connector, P/N 350211-1, mounted on the solder side of the circuit board. The recommended mating connector, P3, is AMP P/N 1-480424-0, utilizing AMP pins P/N 60619-4. J3 pins are labeled on the J3 connector (see Figure 3-4). J3 cabling must be 18 AWG, minimum.

## FRAME GROUND CONNECTOR

The frame ground connector is Faston AMP P/N 61761-2. The recommended mating connector is AMP P/N 62187-1. To realize error rates (see Table 2-2), it must be connected directly to the centrally located system ground via an 18 AWG, minimum, cable.

### 3.5 INTERFACE LINE DESCRIPTIONS

The interface for the TM500 series drive is available in one configuration. It is compatible with industry standard drives. Compatibility is defined as using the same pin assignment where the signal and function are common. Table 3-1 contains pin assignments.

The interface may be connected in the radial or daisy chain configuration (see Figures 3-5 and 3-6).

## INPUT CONTROL SIGNALS

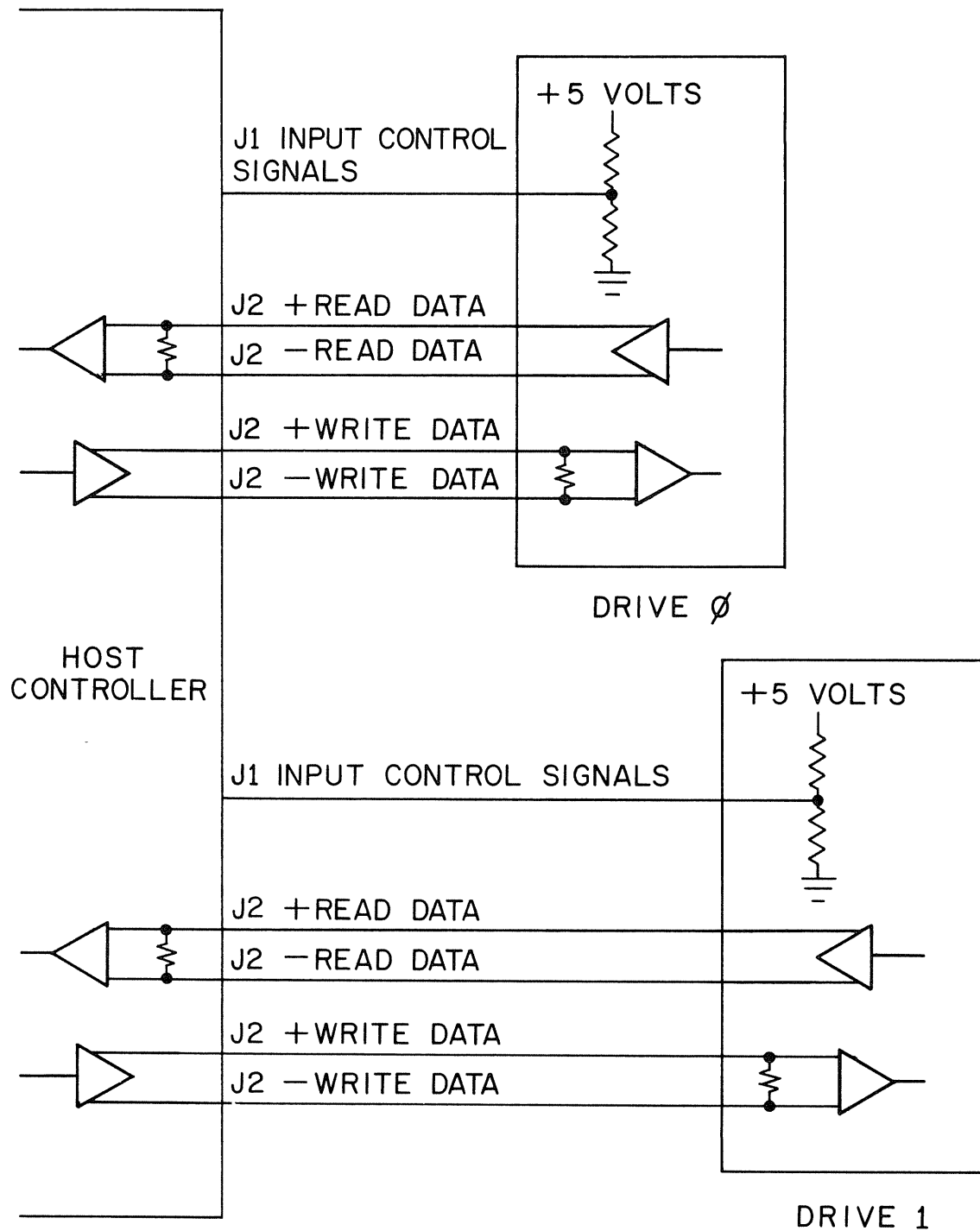
The input control signals are of two kinds: those to be multiplexed in a multiple drive system and those that do the multiplexing. The input control signals to be multiplexed are: Reduced Write Current, Write Gate, Head Select Line 2<sup>0</sup>, Head Select Line 2<sup>1</sup>, Head Select Line 2<sup>2</sup>, Step, and Direction In. The multiplexing signal is Drive Select 0, Drive Select 1, Drive Select 2 or Drive Select 3.

The input signals have the following electrical specifications, as measured at the drive. Figure 3-7 illustrates the recommended circuit.

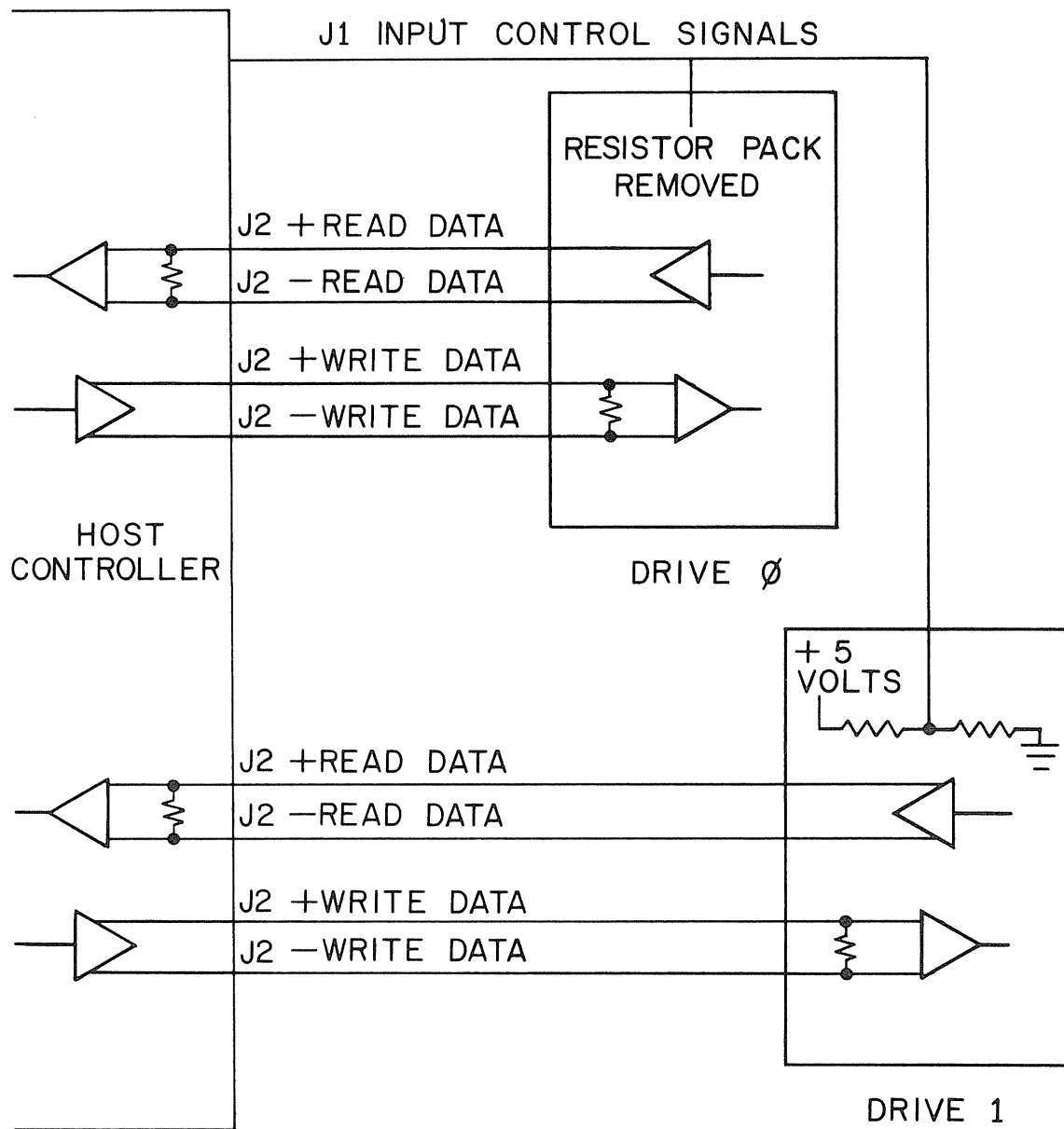
True: 0.0 volt D. C. to 0.4 volt D. C. at  $I = -40$  milliamperes, maximum

False: 2.5 volts D. C. to 5.25 volts D. C. at  $I = 250$  microamperes, maximum (open) <sup>L</sup>

All input signals share a 220/330 ohm resistor pack for line termination. Only the last drive in the chain should have the resistor pack installed.

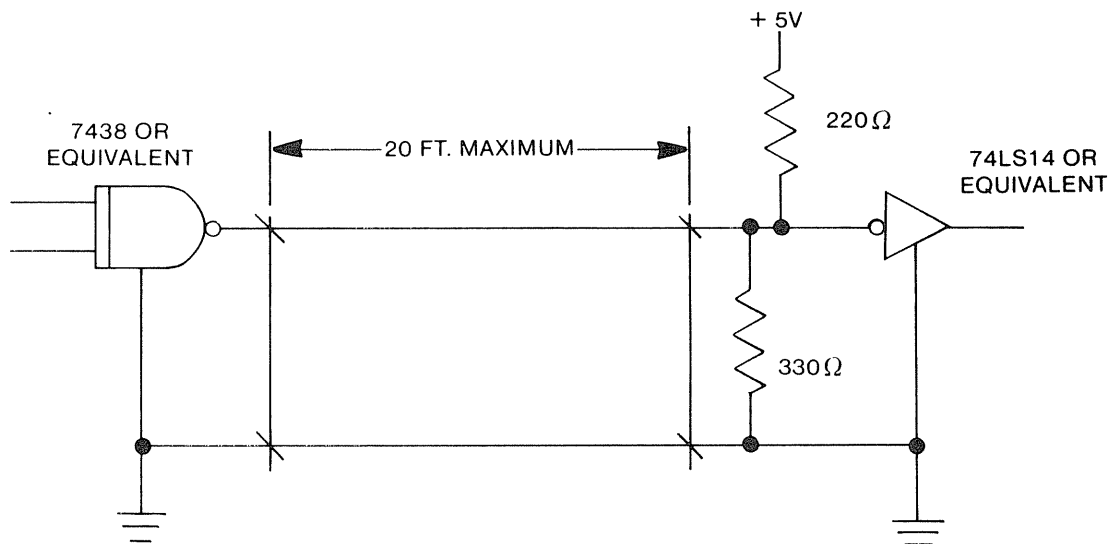


**FIGURE 3-5  
RADIAL CONFIGURATION**



**FIGURE 3-6**  
**DAISY CHAIN CONTROL LINES**





**FIGURE 3-7**  
**CONTROL SIGNAL/DRIVER RECEIVER CIRCUIT COMBINATION**

## WRITE GATE

The active state of this signal or logical zero level enables write data to be written on the disk. The inactive state of this signal enables the data to be transferred from the drive. In addition, the inactive state enables the step pulse to step the read/write actuator.

## HEAD SELECT LINES $2^0$ , $2^1$ , $2^2$

These three lines provide for the selection of each read/write head in a binary coded sequence. Head Select Line  $2^0$  is the least significant line. The heads are numbered 0 through 5. When all Head Select Lines are false, Head 0 is selected. Table 3-2 describes which head is selected for the head select lines.

**TABLE 3-2  
HEAD SELECT LINES**

| Head Select Line |       |       | Head Selected                        |                              |
|------------------|-------|-------|--------------------------------------|------------------------------|
| $2^2$            | $2^1$ | $2^0$ | Jumper<br>3 PW13 OUT<br>TM501, TM502 | Jumper<br>3 PW13 IN<br>TM503 |
| 1                | 1     | 1     | 0                                    | 0                            |
| 1                | 1     | 0     | 1                                    | 1                            |
| 1                | 0     | 1     | 2                                    | 2                            |
| 1                | 0     | 0     | 3                                    | 3                            |
| 0                | 1     | 1     | 0                                    | 4                            |
| 0                | 1     | 0     | 1                                    | 5                            |
| 0                | 0     | 1     | 2                                    | 0                            |
| 0                | 0     | 0     | 3                                    | 1                            |

Head recovery time (head-to-head select, write-to-read recovery, or read-to-write recovery) is 2.4 microseconds maximum.

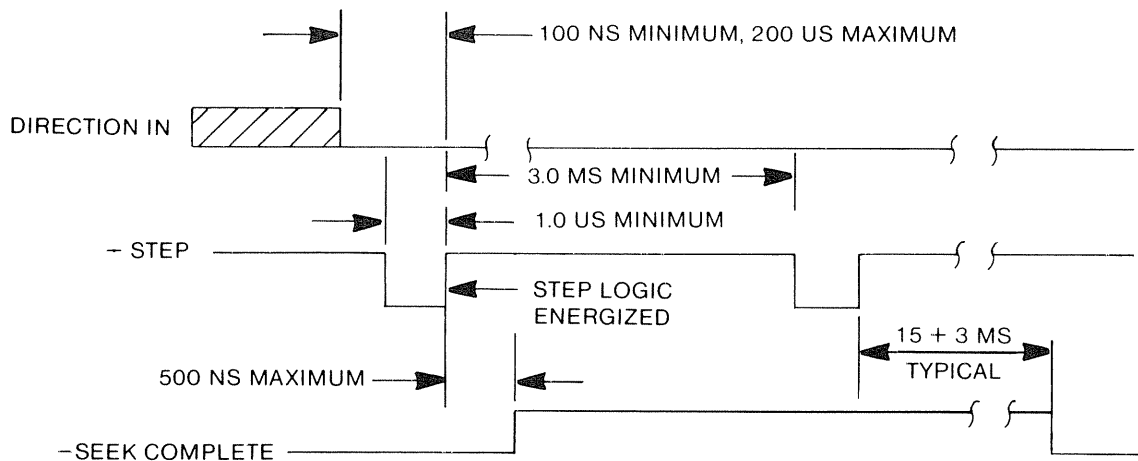
#### STEP

This interface line is a control signal that causes the read/write heads to move with the direction of motion defined by the Direction In line.

The access motion is initiated at the logical true-to-

logical false transition or the trailing edge of this signal pulse. Any change in the Direction in line must be made at least 100 nanoseconds before the true-to-false edge of the step pulse. The quiescent state of this line should be held logically false.

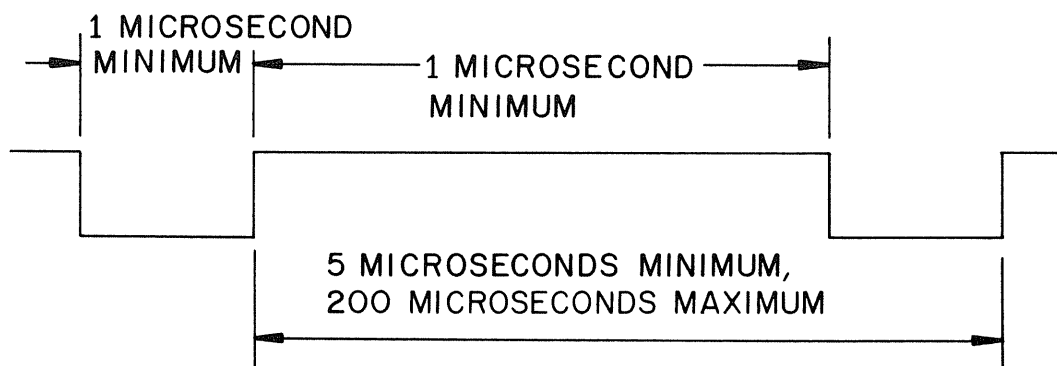
The read/write head moves at the rate of the incoming step pulses. The minimum time between successive steps is three milliseconds, except during execution of a buffered seek. The minimum pulse width is one microsecond. Figure 3-8 illustrates the step timing.



**FIGURE 3-8  
STEP MODE TIMING**

## BUFFERED SEEK

The buffered seek uses an onboard microprocessor that calculates the most efficient seek algorithm for the user. The user need only issue step pulses in accordance with the timing shown (see Figure 3-9). Step pulses are issued in a 1:1 ratio to the cylinders moved. If more pulses are issued than there are cylinders left to move, the heads soft stop at the last cylinder.



**FIGURE 3-9  
BUFFERED SEEK STEP PULSES**

## DIRECTION IN

This signal defines the direction of motion of the read/write head when the Step line is pulsed. An open circuit or logical false defines the direction as "out". If a pulse is applied to the Step line, the read/write heads move away from the center of the disk. If this line is true, the direction is defined as "in", and the read/write heads move in toward the center of the disk.

Seek Complete must be true prior to changing directions and the application of additional step pulses.

## REDUCED WRITE CURRENT

The Reduced Write Current input line is terminated, but is not used in the TM500 series drives. The microprocessor automatically switches write current.

## DRIVE SELECT 0 THROUGH DRIVE SELECT 3

These control signals enable the selected drive's input receivers and output drivers. When logically false, the output drivers are open circuits and the input receivers do not acknowledge signals presented to them.

Selecting the appropriate jumper block at W9 through W12 determines which select line activates the drive.

### NOTE

Only one drive may be selected at a time.

## OUTPUT CONTROL SIGNALS

The output control signals are driven with an open collector output stage capable of sinking a maximum of 40 milliamperes in a true state, with a maximum voltage of 0.4 volt measured at the driver. When the line driver is in the false state, the driver transistor is off, and the collector cutoff is a maximum of 250 microamperes.

All J1 output lines are enabled by the respective Drive Select lines.

## SEEK COMPLETE

The Seek Complete signal goes true when the read/write heads have settled on the final track at the end of a seek. Reading or writing should not be attempted when Seek Complete is false.

Seek Complete goes false:

1. When a recalibration sequence is initiated by the microprocessor at power on because the read/write heads are not over Track 0.
2. 500 nanoseconds, maximum, after the trailing edge of a step pulse or a series of step pulses.
3. When power is momentarily lost, Seek Complete is false when power is restored and remains false until an automatic recalibration is completed.

## TRACK 0

The Track 0 signal indicates a true state only when the drive's read/write heads are positioned at Track 0, the outermost data track.

## FAULT

The Fault signal is used to indicate a condition exists in the drive that could cause improper writing on the disk. When this line is true, further writing is inhibited, as are other drive functions, until the condition is corrected.

This condition is caused by either the +12 volt or +5 volt supply dropping below the specified limits, and on power up until a successful recalibration sequence is completed.

## INDEX

The Index signal is provided once each revolution, 16.7 milliseconds nominal, to indicate the beginning of the track. Normally, this signal is false and makes the transition to true to indicate Index. Only the transition from logical false to logical true is valid.

## READY

When true, the Ready signal, together with Seek Complete, indicates that the drive is ready to read, write or seek, and the I/O signals are valid. When this line is false, all controller-initiated functions are inhibited.

The typical time after power on for Ready to be true is fifteen seconds. Track 0, Seek Complete, and Ready come true sequentially during power on.

## SELECT STATUS

A Status line is provided at the J2/P2 connector to inform the host system of the selection status of the drive.

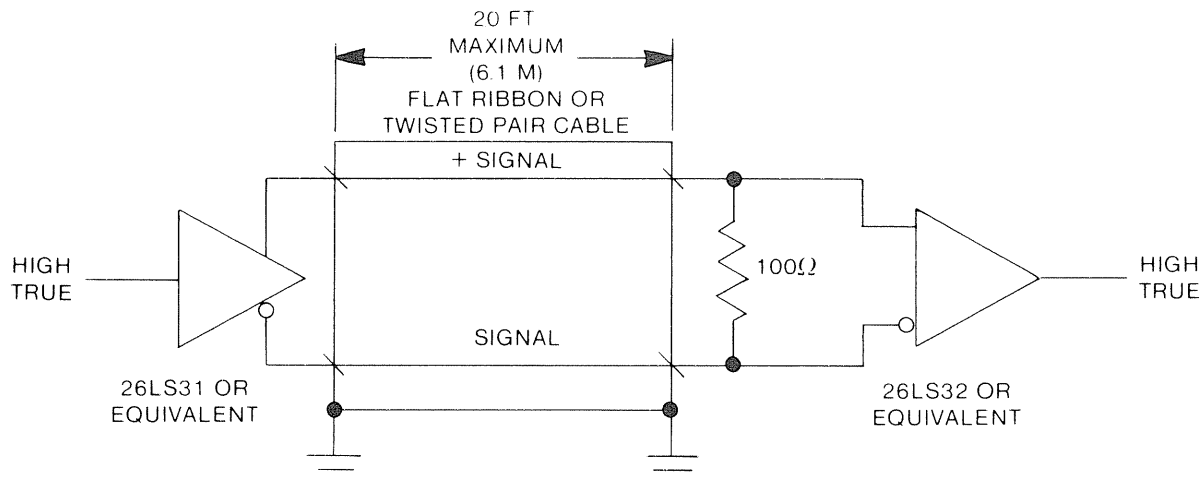
The Drive Selected line is driven by a TTL open col-

lector drive (see Figure 3-7). This signal goes active only when the drive is programmed as Drive X,  $X = 0, 1, 2$ , or  $3$ , by programming the shunt on the drive, and the Drive Select X line at J1/P1 is activated by the host system.

## DATA TRANSFER SIGNALS

All lines associated with the transfer of data between the drive and the host system are differential in nature and may be multiplexed. These lines are provided at the J2/P2 connector on all drives. Signal levels are defined by RS-422A.

Two pairs of balanced lines are used for the transfer of data: MFM Write Data and MFM Read Data. Figure 3-10 illustrates the driver/receiver combination used with the drive for data transfer signals.



**FIGURE 3-10**  
**DATA TRANSFER LINE DRIVER RECEIVER**

## **MFM WRITE DATA**

This is a differential pair of lines that define the flux transition to be written on the track. The transition of the +MFM Write Data line going more positive than the -MFM Write Data line causes a flux reversal on the track if Write Gate is active. This signal must be driven to an inactive state, +MFM Write Data more negative than -MFM Write Data, by the host system when in a read mode.

The delay from the leading edge of Write Gate to the Write Data pulse is 400 nanoseconds, maximum.

## **MFM READ DATA**

The data recovered by reading a prerecorded track is transmitted to the host system via the differential pair of MFM Read Data lines. The transition of the +MFM Read Data line going more positive than the -MFM Read Data line represents a flux reversal on the track of the selected head.

### **3.6 DRIVE ADDRESS AND OPTION SELECTION**

The drive address and option selection is determined by the programmable jumper blocks located on the logic circuit board. If jumper configurations are changed, power should be cycled off and on, so that the microprocessor can recognize the new configuration.

The option programming guide is contained in Table 3-3.

### **3.7 SHIPPING PACK AND HANDLING**

Figures 3-11 through 3-13 provide basic information on recommended design guidelines for packaging systems.

From various drop tests conducted, it has been established that drives subjected to shock loads in excess of twenty G's may be damaged and consequently not meet published performance specifications for data reliability, margins, and function.

In order to avoid media or head damage, it is recommended that:

1. Drive mounting designs incorporate some type of shock dampening consideration.
2. Shipping cartons protect the drive within the system to withstand twenty G's.
3. Individual drives are handled carefully, e.g., receiving and in-process personnel are properly trained, surface mats are used on working surfaces to prevent the possibility of "handling shock," and padding is placed on racks and carts.

Please emphasize the critical aspects of handling these drives to all concerned people. In addition, Tandon provides technical assistance on packing and handling to customers upon request.

**TABLE 3-3**  
**OPTION PROGRAMMING GUIDE**

| <b>W1-W3<br/>Jumper</b> | <b>Function</b>  | <b>Factory<br/>Programmed</b> | <b>Usage</b>  |
|-------------------------|------------------|-------------------------------|---|
| W1                      | Track Fault      | O                             | Install for excess track fault.                                       |
| W2                      | Test             | O                             | Install for factory test.   |
| W3                      | Disable Limit    | O                             | Install to disable soft limits.                                       |
| W4                      | Spin Select      | O                             | Install for spin select.  |
| W5                      | Tracks           | S                             | Install for standard version only.                                    |
| W6                      | Motor Type       | —                             | Install for Type 1.8 <sup>0</sup> motor.                              |
| RTW7                    | Read Terminator  | I                             | Close only at end drive of daisy chain data. Closed for radial data.  |
| WTW8                    | Write Terminator | I                             |   |
| S4W9                    | Drive Select 4   | O                             | Install one of four plugs only.<br>Plug corresponds to drive address. |
| S3W10                   | Drive Select 3   | O                             |   |
| S2W11                   | Drive Select 2   | O                             |   |
| S1W12                   | Drive Select 1   | I                             |   |
| 3PW13                   | Three Disk       | 503                           | Close for Model TM503 only.   |
| U22                     | Terminator Pack  | I                             | Install in end drive of daisy chain.                                  |

**NOTES:**

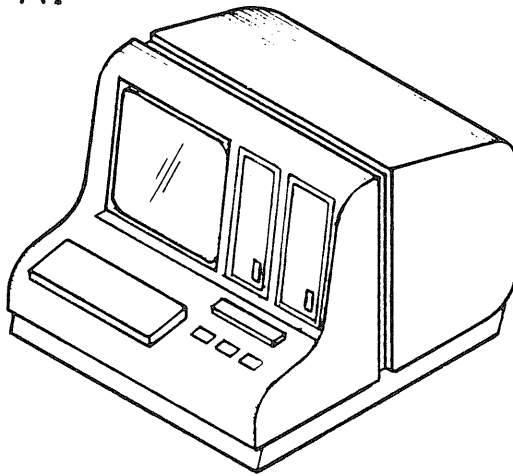
O = Omit

I = Install

S = Close jumper for standard version only.

503 = Close jumper for TM503 only.

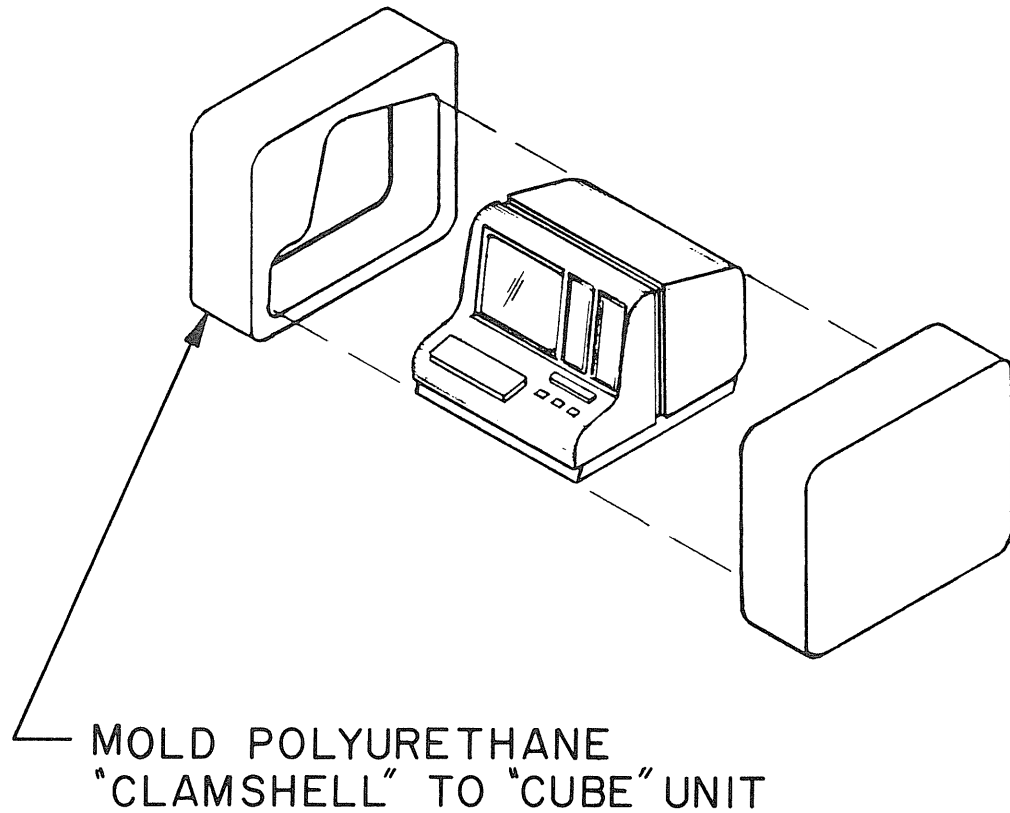
STEP A.



ENCLOSE UNIT IN POLY BAG  
TO AVOID SURFACE  
SCRATCHES AND  
OTHER DAMAGE



STEP B.



MOLD POLYURETHANE  
"CLAMSHELL" TO "CUBE" UNIT

FIGURE 3-11  
RECOMMENDED SHIPPING PACK DESIGN,  
CONFIGURATION 1



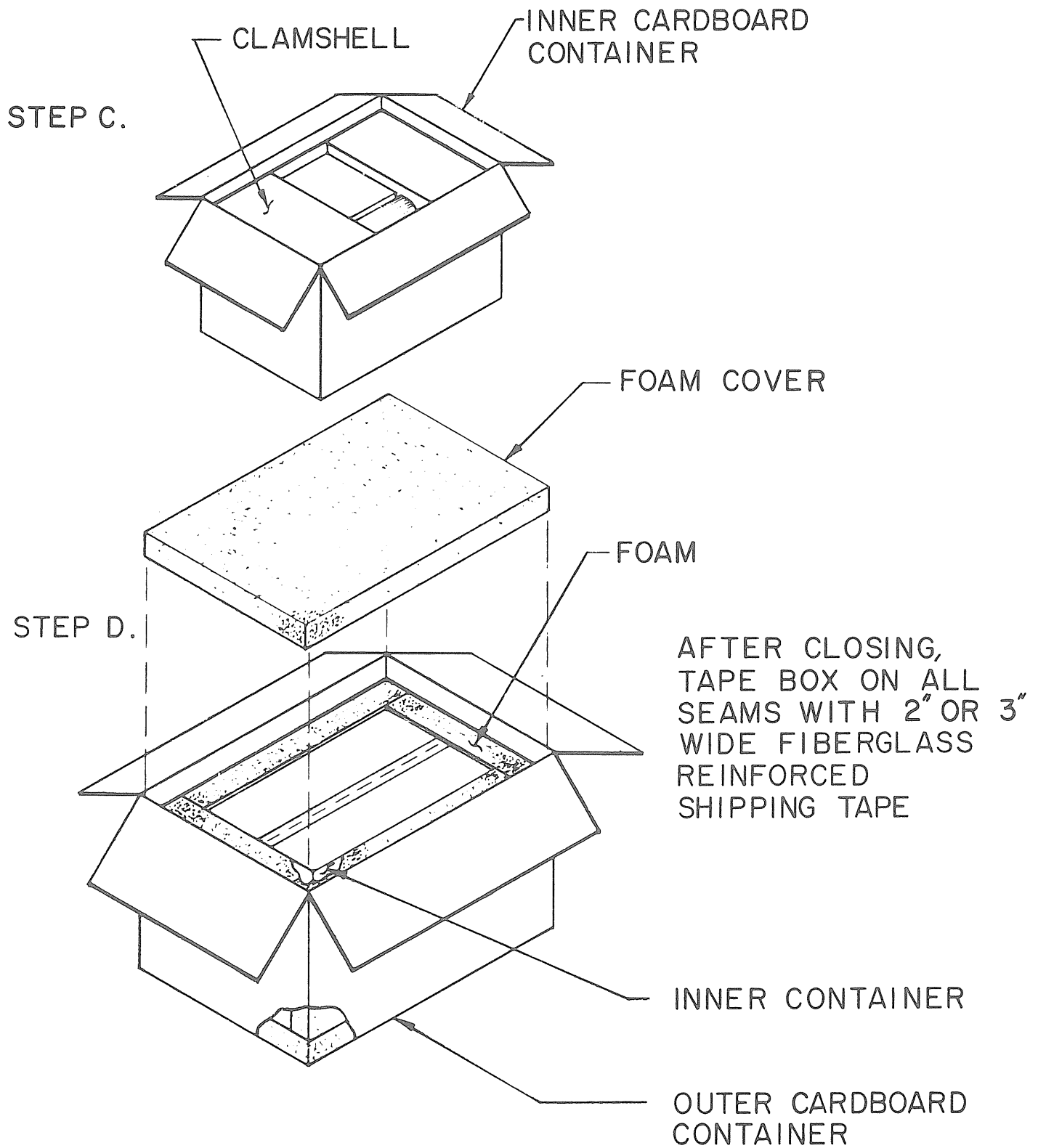
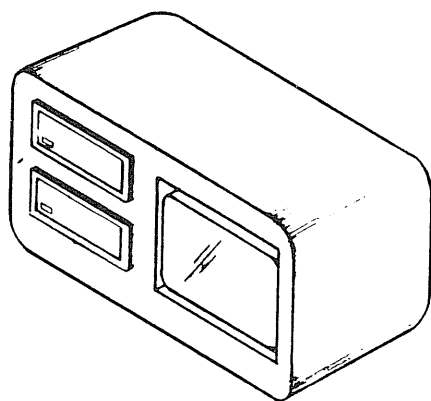


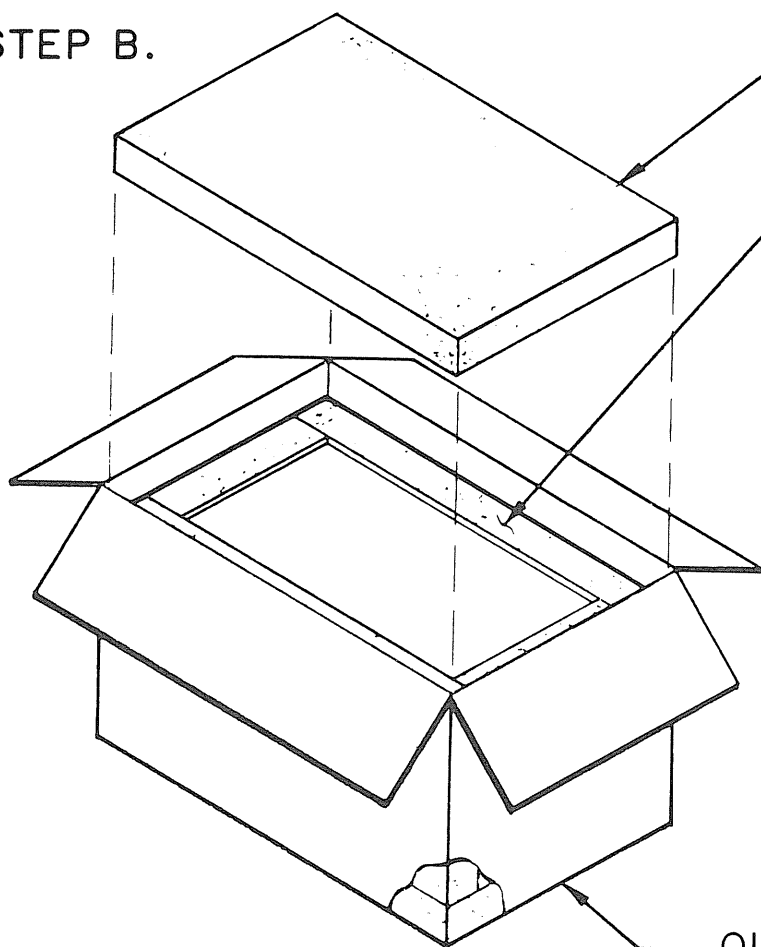
FIGURE 3-11 (CONTINUED)  
RECOMMENDED SHIPPING PACK DESIGN,  
CONFIGURATION 1

STEP A.



ENCLOSE UNIT IN POLY BAG  
TO AVOID SURFACE  
SCRATCHES AND  
OTHER DAMAGE

STEP B.



FOAM COVER

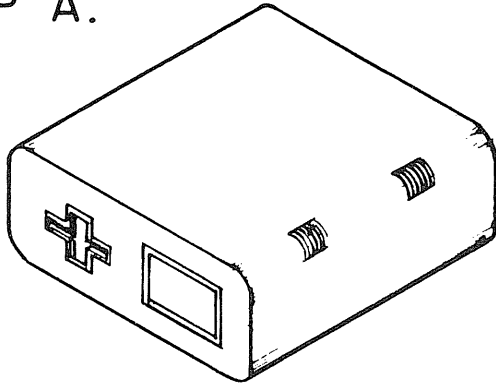
FOAM

AFTER CLOSING,  
TAPE BOX ON ALL  
SEAMS WITH 2" OR 3"  
WIDE FIBERGLASS  
REINFORCED  
SHIPPING TAPE

OUTER CARDBOARD  
CONTAINER

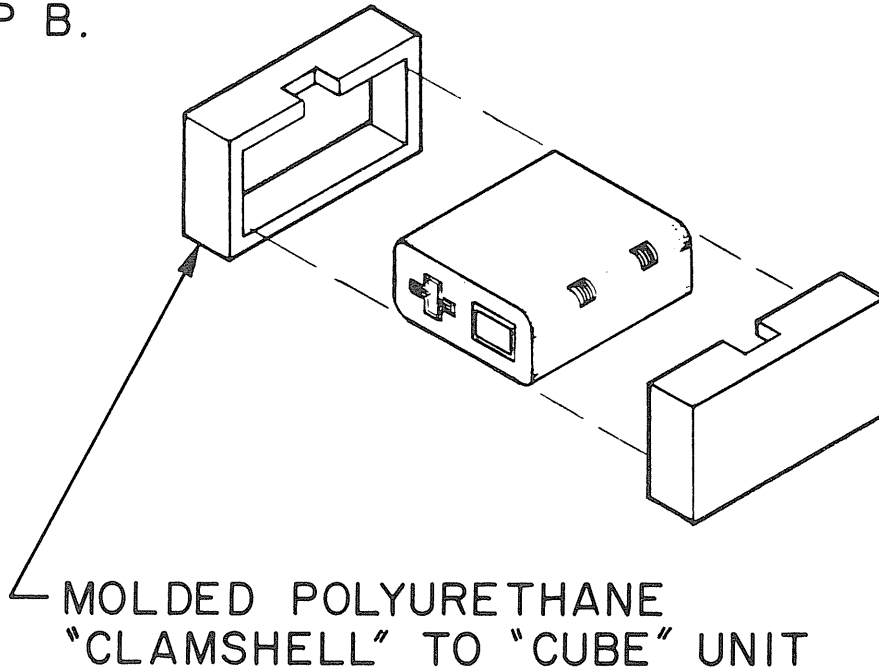
FIGURE 3-12  
RECOMMENDED SHIPPING PACK DESIGN,  
CONFIGURATION 2

STEP A.



ENCLOSE UNIT IN POLY BAG  
TO AVOID SURFACE  
SCRATCHES AND  
OTHER DAMAGE

STEP B.



MOLDED POLYURETHANE  
"CLAMSHELL" TO "CUBE" UNIT

FIGURE 3-13  
RECOMMENDED SHIPPING PACK DESIGN,  
CONFIGURATION 3

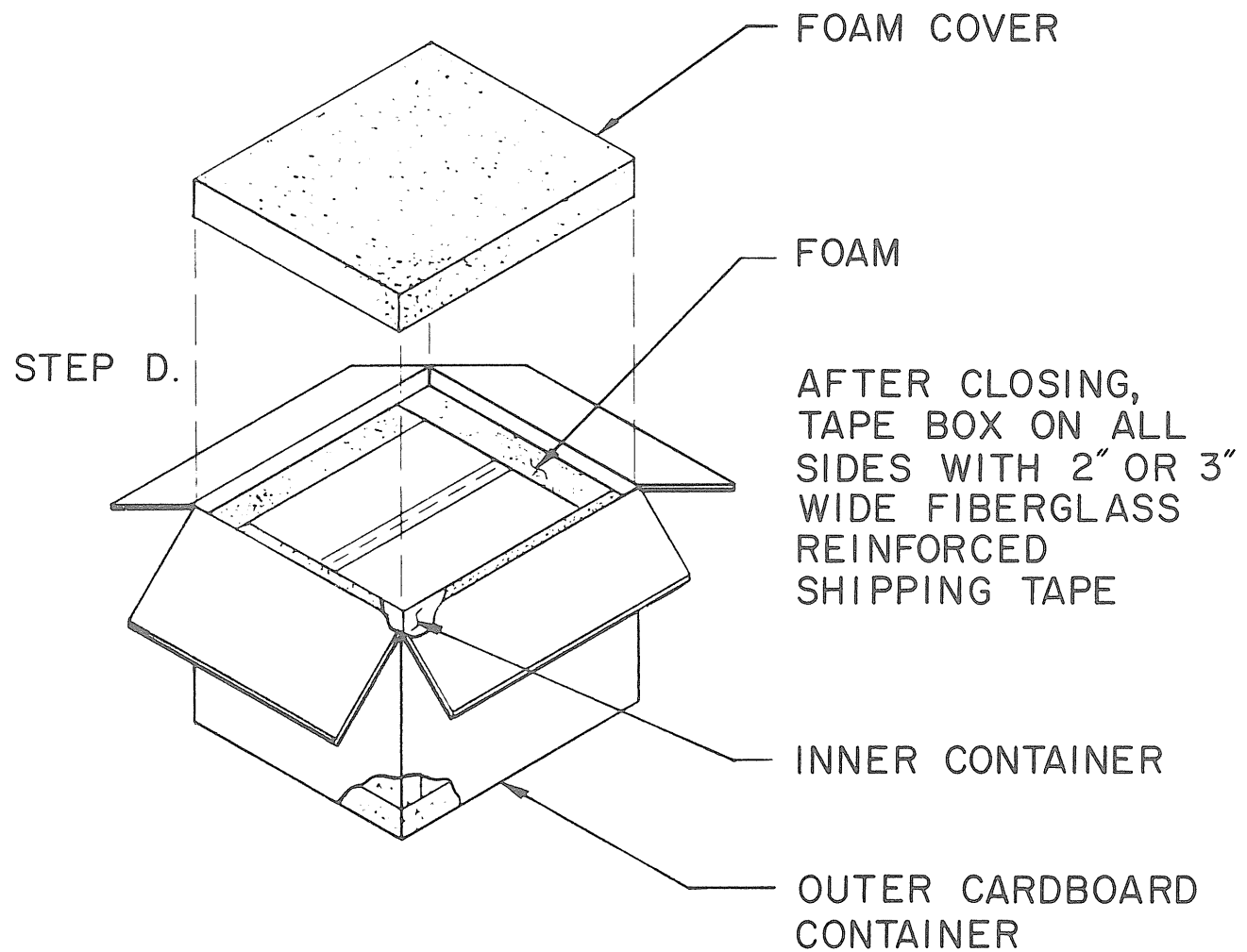
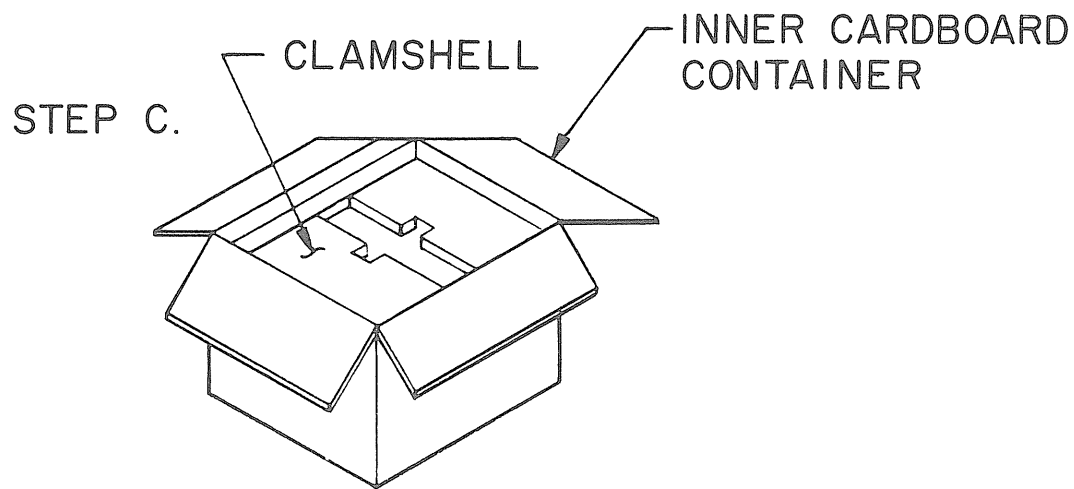
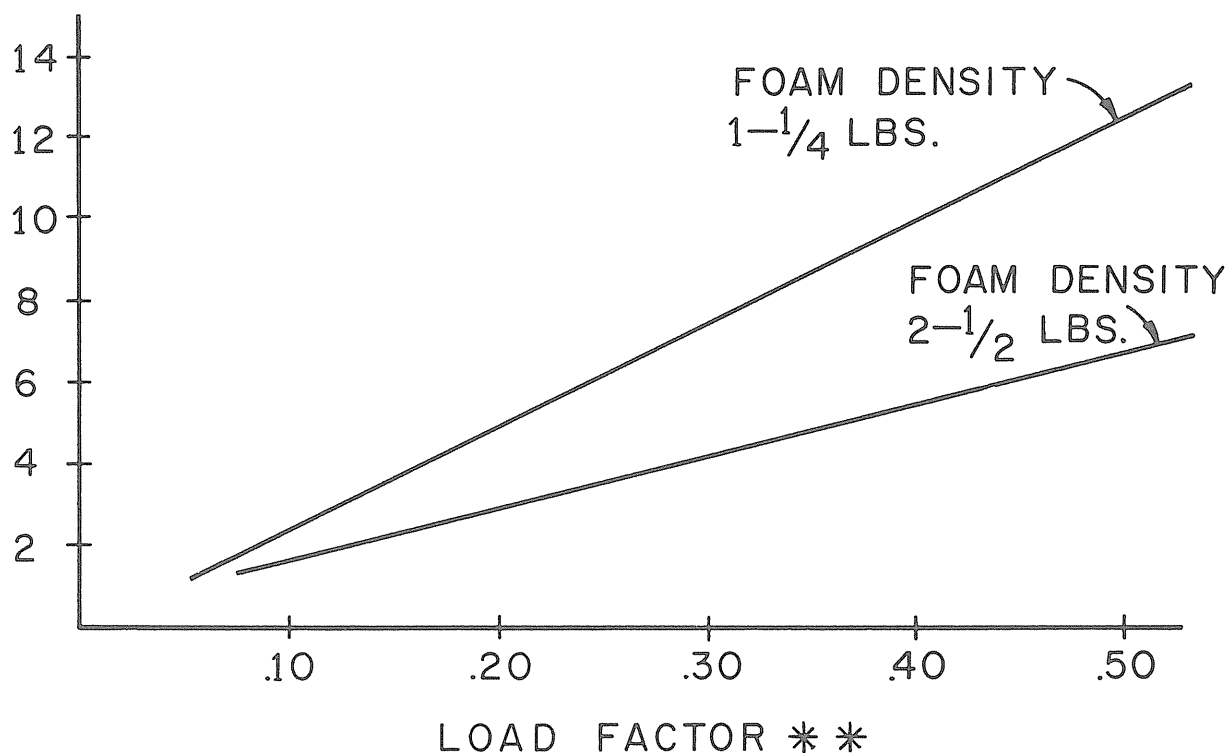


FIGURE 3-13 (CONTINUED)  
RECOMMENDED SHIPPING PACK DESIGN,  
CONFIGURATION 3

POLYETHER/POLYESTER  
FOAM  
\*



NOTE

THE GRAPH GIVES FOAM THICKNESSES TO SATISFY 10 G MINIMUM SHOCK LOADS ON STANDARD 30-INCH DROP TEST. HENCE, ALL PACKAGE DESIGNS SHOULD BE TESTED TO VERIFY THEIR ULTIMATE PERFORMANCE.

\* THICKNESS IS IN INCHES.

\*\* THE LOAD FACTOR IS IN LBS. PER SQ. INCH.

THE FORMULA IS :  $\text{LOAD FACTOR} = \frac{\text{UNIT'S WEIGHT (LBS.)}}{\text{SMALLEST SIDE OF UNIT (SQ. INCHES)}}$

FIGURE 3-14  
LOAD GRAPH



Fig. 1

# SECTION 4

## TROUBLESHOOTING GUIDE AND REPLACEMENT PROCEDURE

### INTRODUCTION

This section is designed to help locate and correct failures related to the drive. Table 4-2 is a troubleshooting guide outlining the problem, its possible cause, and the recommended action. This section also contains parts removal, replacement, and adjustment procedures.

In all cases, the power supply voltages should be checked before proceeding.

#### 4.1 TROUBLESHOOTING GUIDE

##### TEST EQUIPMENT

The following test equipment, or its equivalent, is recommended:

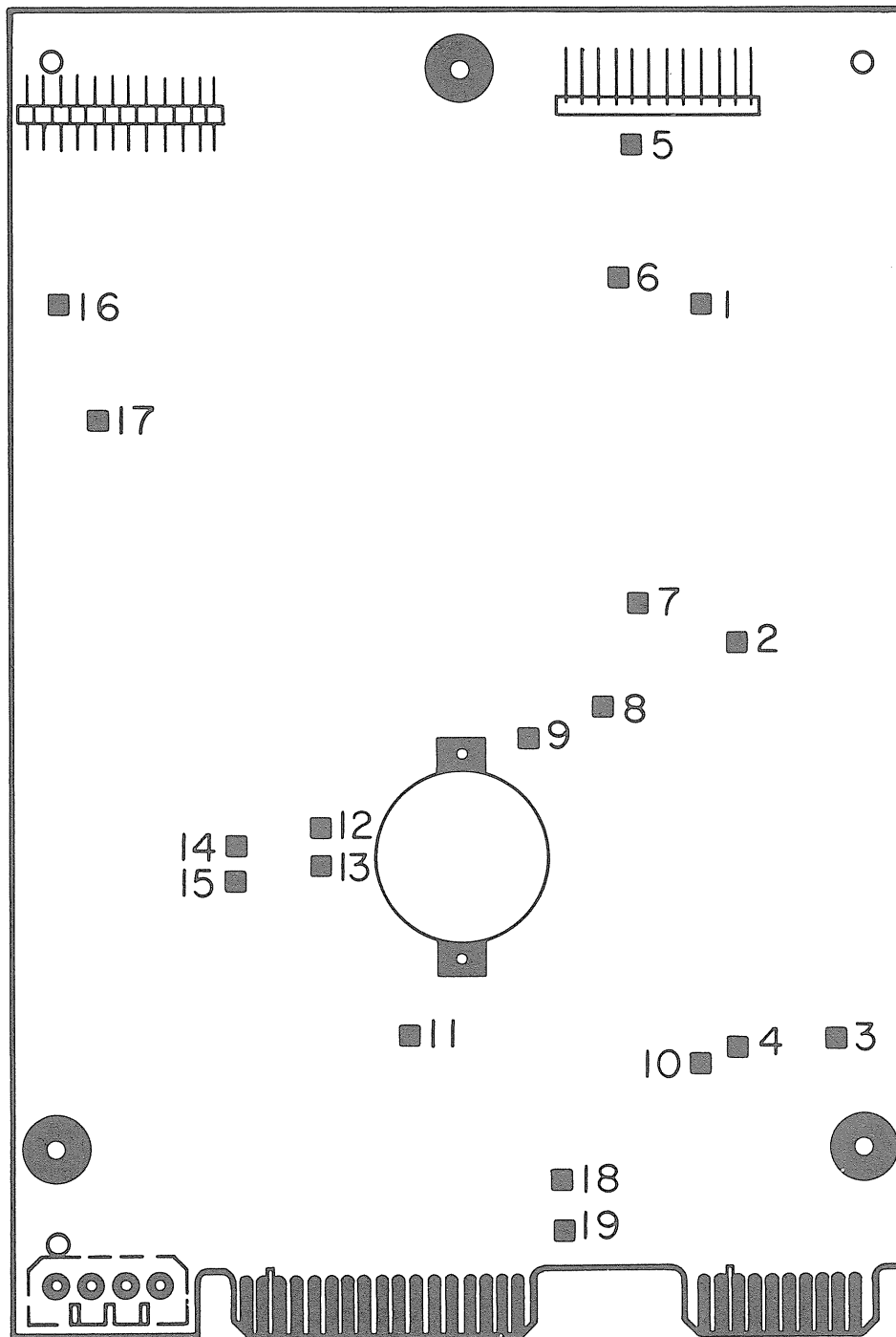
1. Oscilloscope, Tektronix 465—vertical and horizontal sensitivity plus three percent specified accuracy—with three 10X probes, each with individual ground leads.
2. Counter Timer, Monsanto Model 100B.
3. Digital Voltmeter (DVM), John Fluke Model 800A.

##### TEST POINTS

Table 4-1 contains each test point by function name. All test points referred to are on the logic board. Figure 4-1 illustrates their locations.

**TABLE 4-1  
TEST POINTS**

| Test Point                       | Signal                       |
|----------------------------------|------------------------------|
| 1, 4, 9, 11, 12, 13, 16, 17..... | Logic Ground                 |
| 2.....                           | Direction                    |
| 3.....                           | Index                        |
| 5.....                           | Photo Sensor, Track 0        |
| 6.....                           | Track 0                      |
| 7.....                           | Step                         |
| 8.....                           | Seek Complete                |
| 10.....                          | Pulse Read Data              |
| 14.....                          | – Analog Read Data           |
| 15.....                          | + Analog Read Data           |
| 18.....                          | Reserved + 5 volts D. C.     |
| 19.....                          | Cable Detect + 5 volts D. C. |



**FIGURE 4-1**  
**TEST POINT LOCATIONS**



**TABLE 4-2  
TROUBLESHOOTING GUIDE**

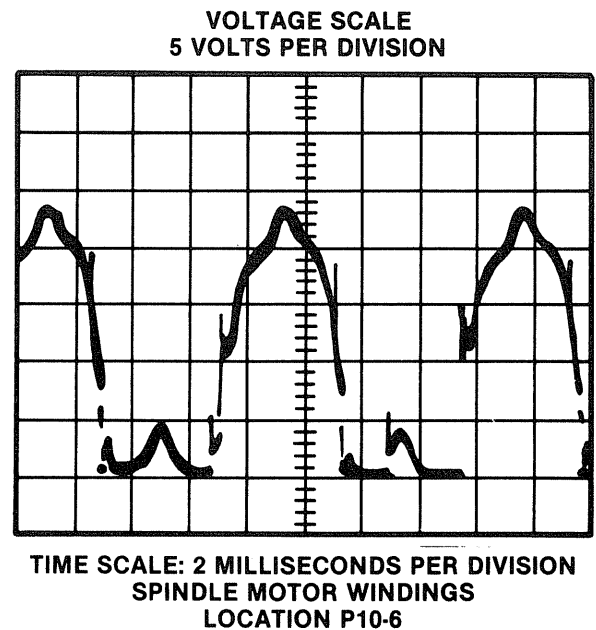
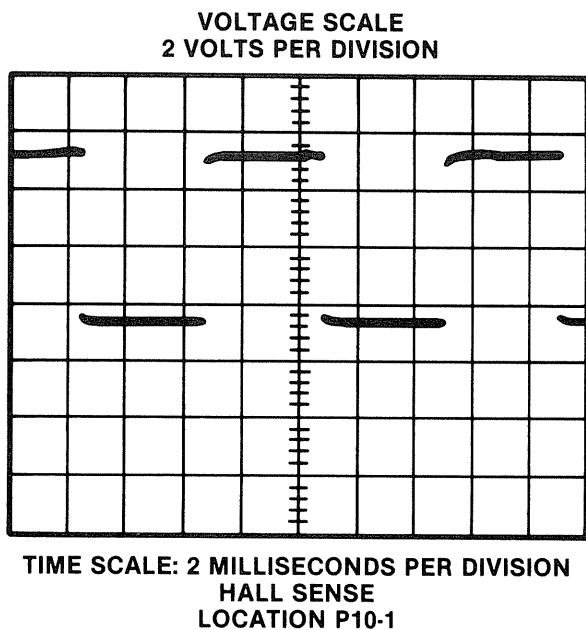
| Problem           | Possible Cause  | Recommended Action   |
|-------------------|---|--|
| No activity lamp. | Not selected.<br><br>Lamp not plugged in.<br><br>Lamp faulty.<br><br>Logic board faulty.  | Check for correct drive select jumper.<br><br>Check Connector P-7.<br><br>Replace lamp.<br><br>Replace logic board.  |
| No index.         | Not selected.<br><br>Index sensor not plugged in.<br><br>Index sensor misadjusted.<br><br>Index sensor faulty.<br><br>Logic board faulty.             | Check drive select jumper.<br><br>Check Connector P-5<br><br>Readjust Index sensor.<br><br>Replace sensor.<br><br>Replace logic board.                                   |
| No Track 0.       | Not selected.<br><br>Track 0 sensor not plugged in.<br><br>Track 0 sensor misadjusted.<br><br>Track 0 sensor faulty.<br><br>Logic board faulty.       | Check drive select jumper.<br><br>Check Connector P-9.<br><br>Readjust Track 0 sensor.<br><br>Replace Track 0 sensor.<br><br>Replace logic board.                        |
| Drive not ready.  | Not selected.<br><br>No index.<br><br>No Track 0.<br><br>Motor not up to speed or not turning.<br><br>Drive does not seek.<br>Drive does not restore. | Check drive select jumper.<br><br>See "No index."<br><br>See "No Track 0."<br><br>See "Motor not up to speed, not turning."<br><br>See "Drive does not seek or restore." |

**TABLE 4-2 (CONTINUED)  
TROUBLESHOOTING GUIDE**

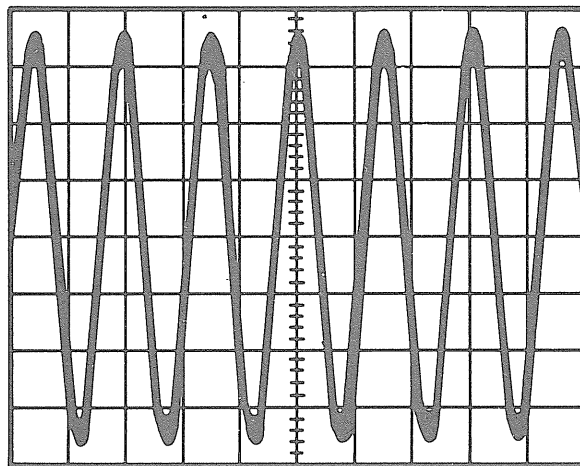
| Problem                             | Possible Cause  | Recommended Action  |
|-------------------------------------|---|---|
| Drive does not seek or restore.     | <p>Drive not selected.</p> <p>Stepper motor not plugged in.</p> <p>Logic board faulty.</p> <p>Spindle motor speed misadjusted.</p> <p>Stepper motor faulty.</p> <p>Foreign object interfering with positioning arm or damper.</p> | <p>Check drive select jumper.</p> <p>Check Connector P-8.</p> <p>Replace logic board.</p> <p>Readjust spindle motor's speed.</p> <p>Return to factory for repair.</p> <p>Remove foreign object.</p>   |
| Motor not up to speed, not turning. | <p>Spindle circuit board not plugged in.</p> <p>Spindle motor not plugged in.</p> <p>Faulty Spindle circuit board (see Figure 4-2).</p> <p>Motor not up to speed.</p> <p>Motor faulty.</p> <p>Brake misadjusted.</p>              | <p>Check Connector P-12.</p> <p>Check Connectors P-10 and P-11.</p> <p>Replace circuit board.</p> <p>Adjust spindle speed.</p> <p>Return to factory for repair.</p> <p>Readjust brake.</p>  |
| Does not read.                      | <p>Not selected.</p> <p>Heads not selected.</p> <p>Head cable not plugged in.</p> <p>Logic board faulty (see Figure 4-3).</p> <p>Defective head.</p>  | <p>Check drive select jumper.</p> <p>Check head select interface lines.</p> <p>Check jumper 3P W13, installed for Model Number TM503 (see Table 3-2, page 3-10).</p> <p>Check Connector P-6.</p> <p>Replace logic board.</p> <p>Return to factory for repair.</p> |

**TABLE 4-2 (CONTINUED)  
TROUBLESHOOTING GUIDE**

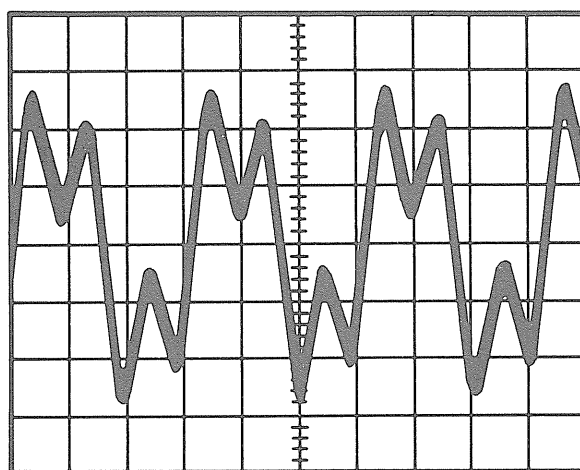
| Problem         | Possible Cause   | Recommended Action  |
|-----------------|--|---|
| Does not read.  | Improper position.<br><br>Drive not ready.   | See "Drive does not seek or restore."<br><br>See "Drive not ready."   |
| Does not write. | No Write Gate for writing.<br><br>Not selected.<br><br>Heads not selected.<br><br>Head cable not plugged in.<br><br>Logic board faulty.<br><br>Defective head.<br><br>Drive not ready. | Check Write Gate interface line.<br><br>Check drive select jumper.<br><br>See "Does not read."<br><br>Check Connector P-6.<br><br>Replace logic board.<br><br>Return to factory for repair.<br><br>See "Drive not ready." |



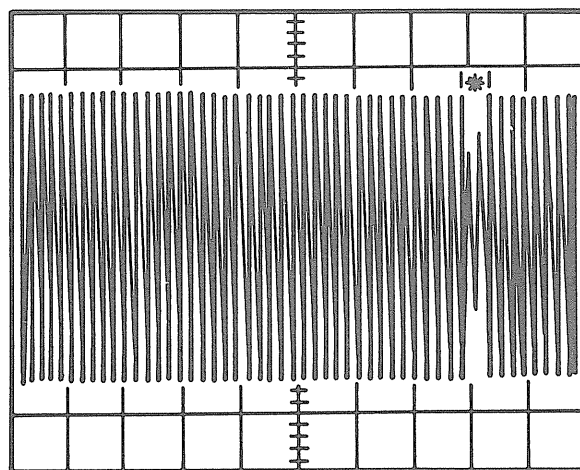
**FIGURE 4-2  
SPINDLE MOTOR CIRCUIT BOARD WAVEFORMS**



ALL ZERO PATTERN



ALTERNATING ONES AND ZEROS



\* BIT DROPOUT CAUSED BY MEDIA DEFECT

FIGURE 4-3  
READ DATA WAVEFORMS 15 TPR + AND 14 TPR —

## 4.2 REPLACEMENT PROCEDURES

Do not rotate spindle motor or head positioning mechanism. Damage to heads and/or media may occur.

Remove power prior to replacing parts.

The following assemblies may be replaced:

Control and Data Circuit Board Assembly  
Front Panel L.E.D. Assembly  
Linear Brake Assembly  
Front Panel  
Index Assembly  
Spindle Control Circuit Board Assembly  
Frame Assembly  
Track 0 Sensor Assembly

### TOOLS REQUIRED

Number 2 Phillips screwdriver  
3/16" nut driver  
5/16" nut driver  
.008" or .010" feeler gauge

### CONTROL AND DATA CIRCUIT BOARD ASSEMBLY

#### REMOVAL

To remove the Control and Data Circuit Board Assembly:

1. Remove the three screws that attach the circuit board to the chassis.
2. Remove the Connectors J4 and J5 from the circuit board.
3. Slide the circuit board toward the side of the frame, lift up on the free side of it, and remove remaining connectors.

### REPLACEMENT

To replace this assembly, reverse Steps 1 through 3.

### FRONT PANEL L.E.D. ASSEMBLY

#### REMOVAL

To remove the Front Panel L.E.D. Assembly:

1. Remove the Control and Data Circuit Board Assembly.
2. Remove the L.E.D. Assembly by pressing the center of the Front Panel L.E.D. Assembly with a blunt tool.

#### NOTE

Press from the inside to the outside of the panel.

3. Remove the square retainer from the assembly.

### REPLACEMENT

To replace this assembly, reverse Steps 1 through 3.

### LINEAR BRAKE ASSEMBLY

#### REMOVAL

To remove the Linear Brake Assembly:

1. Remove the Control and Data Circuit Board Assembly.
2. Remove Connector J11 from the Spindle Control circuit board.
3. Remove the 5/16-inch nut and washer from the mounting stud.
4. Remove the Linear Brake Assembly.

## REPLACEMENT

To replace the assembly, reverse Steps 1 through 4.

## ADJUSTMENT

To adjust the Linear Brake Assembly:

1. Loosen the 5/16-inch mounting nut.
2. Set the feeler gauge, adjusted to 0.014 inches, between the brake pad and the spindle drive motor rotor.
3. Push the brake pad up against the feeler gauge.
4. Align the brake shoe with the Spindle Drive Motor rotor.
5. Tighten the 5/16-inch mounting nut.
6. Remove the feeler gauge.

## INDEX ASSEMBLY

### REMOVAL

To remove the Index Assembly:

1. Remove the Control and Data Circuit Board Assembly.

2. Loosen the lock nut on the Index Assembly bracket.
3. Unscrew the Index Assembly, and remove.



## REPLACEMENT

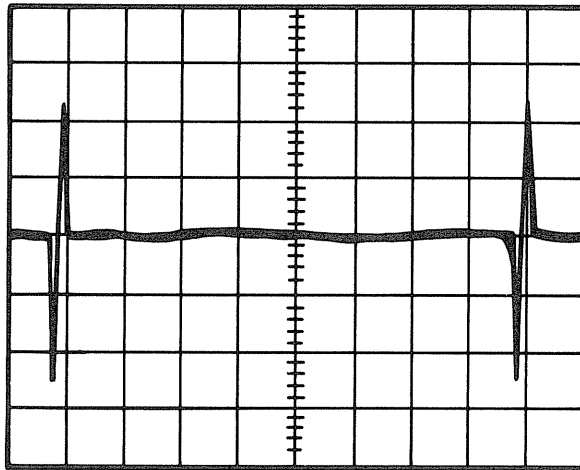
To replace the Index Assembly, reverse Steps 1 through 3.

## ADJUSTMENT

To adjust the Index Assembly:

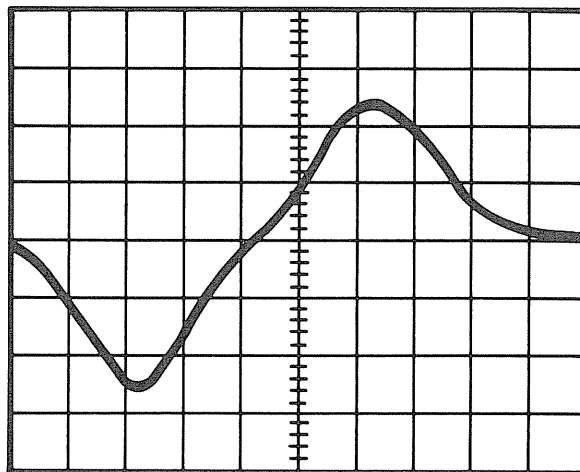
1. Loosen the 3/8-inch locking nut.
2. Adjust the Index Assembly to between 0.008 and 0.010 inches from the motor rotor.
3. Use a 100 megahertz or greater bandwidth oscilloscope.
4. Verify the A. C. signal as seen at U9, Pin 6, is greater than one volt peak to peak (see Figure 4-4).
5. There must not be more than 100 millivolts peak ripple on the base line.

**VOLTAGE SCALE  
1 VOLT PER DIVISION**



**TIME SCALE: 2 MILLISECONDS PER DIVISION  
LOCATION U9-6**

**VOLTAGE SCALE  
1 VOLT PER DIVISION**



**TIME SCALE: 50 MICROSECONDS PER DIVISION  
LOCATION U9-6**

**FIGURE 4-4  
INDEX SENSOR**

## FRONT PANEL

### REMOVAL

To remove the front panel:

1. Wedge a taper shim 0.030 inch under the edge of the front panel.
2. Remove the front panel from the drive frame.
3. Clean the front panel thoroughly with solvent.

### REPLACEMENT

To replace the front panel, apply new double back tape, and fit the panel over the drive frame.

## SPINDLE CONTROL CIRCUIT BOARD ASSEMBLY

### REMOVAL

To remove the Spindle Control Circuit Board Assembly:

1. Remove the Control and Data Circuit Board Assembly.
2. Remove Connectors J10 and J11 from the Spindle Control circuit board.
3. Remove the three screws that attach the board to the chassis.
4. Lift the board straight up and out.

## REPLACEMENT

To replace the board, reverse Steps 2 through 4. When replacing connectors, ensure the proper pin orientation.

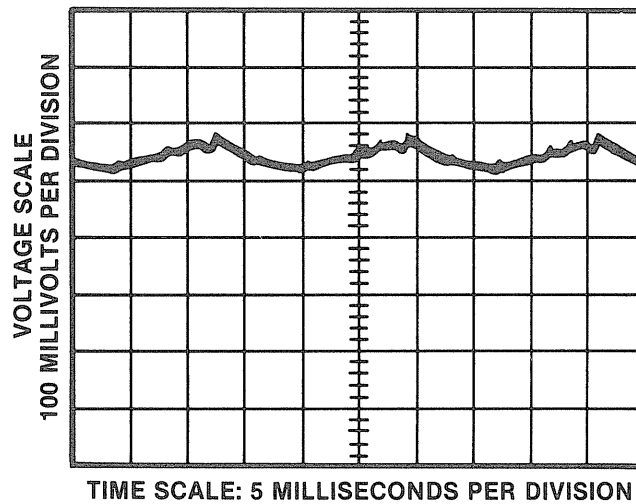


### ADJUSTMENT

To adjust the Spindle Control circuit board:

1. Set potentiometer R23 (closest to interface connectors, single turn) to its approximate center position.
2. Using a dual channel oscilloscope, connect one channel to R26 (resistor lead closest to the power transistor leads on Spindle Control Circuit Board), and set scope to 100 millivolts per division. Connect the other channel of the scope to test point three on the logic board (index), and set the voltage scale at one volt per division and the time base at two milliseconds per division.
3. Apply power to the drive and wait 15 seconds for the spindle motor to come up to speed.
4. While the scope is triggered on index, adjust the speed control potentiometer R5 on the spindle control board (10 turn pot closest to the front of the drive), so the time between index pulses is between 16 and 17 milliseconds.
5. Trigger the scope to the line frequency (60 Hertz). Adjust R5 so index pulses are stationary on the scope screen.
6. Adjust R23 so voltage spikes, both positive and negative going, across R26 are minimal (see Figure 4-5).
7. Repeat Steps 5 and 6 until no further improvement can be made.





**FIGURE 4-5  
VOLTAGE SPIKES**

## FRAME ASSEMBLY

### REMOVAL

To remove the Frame Assembly:

1. Remove the Control and Data circuit board.
2. Remove the Spindle Control circuit board.
3. Remove the three frame nuts and the six washers.
4. Remove the Frame Assembly.

### REPLACEMENT

To replace the Frame Assembly, reverse Steps 1 through 4 above.

## TRACK 0 SENSOR ASSEMBLY

Do not move the positioning mechanism or Track 0 stop.

### REMOVAL

To remove the Track 0 Sensor Assembly:

1. Remove the Circuit Board Assembly.
2. Loosen the screw on the Track 0 Sensor Assembly mount.
3. Swing the mount clear, and remove the screw that holds the Track 0 Sensor Assembly.

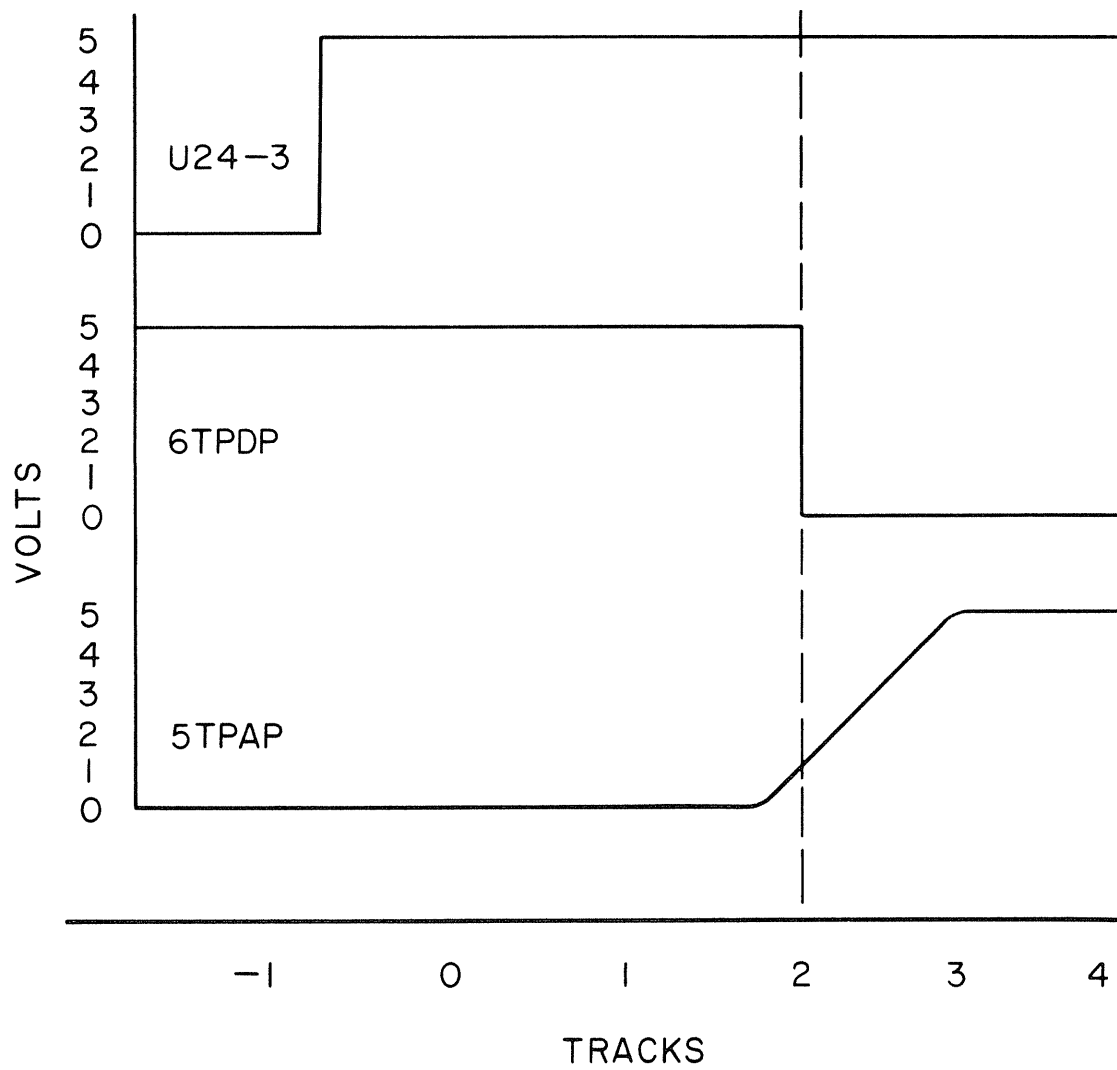
### REPLACEMENT

To replace the Track 0 Sensor Assembly, reverse Steps 1 through 3 above.

### ADJUSTMENT

To adjust the Track 0 Sensor Assembly:

1. Loosen the screw.
2. Adjust the sensor so that it is  $2.5 \pm 1$  volt at Test Point 5, while stepping out from Track 5 to Track 0, and the positioner is on Track 2 plus or minus one track.
3. Verify that Test Point 6 changes logic level at Track 2, plus or minus one track (see Figure 4-6).



**FIGURE 4-6**  
**TRACK 0 ADJUSTMENT**

# **APPENDIX A**

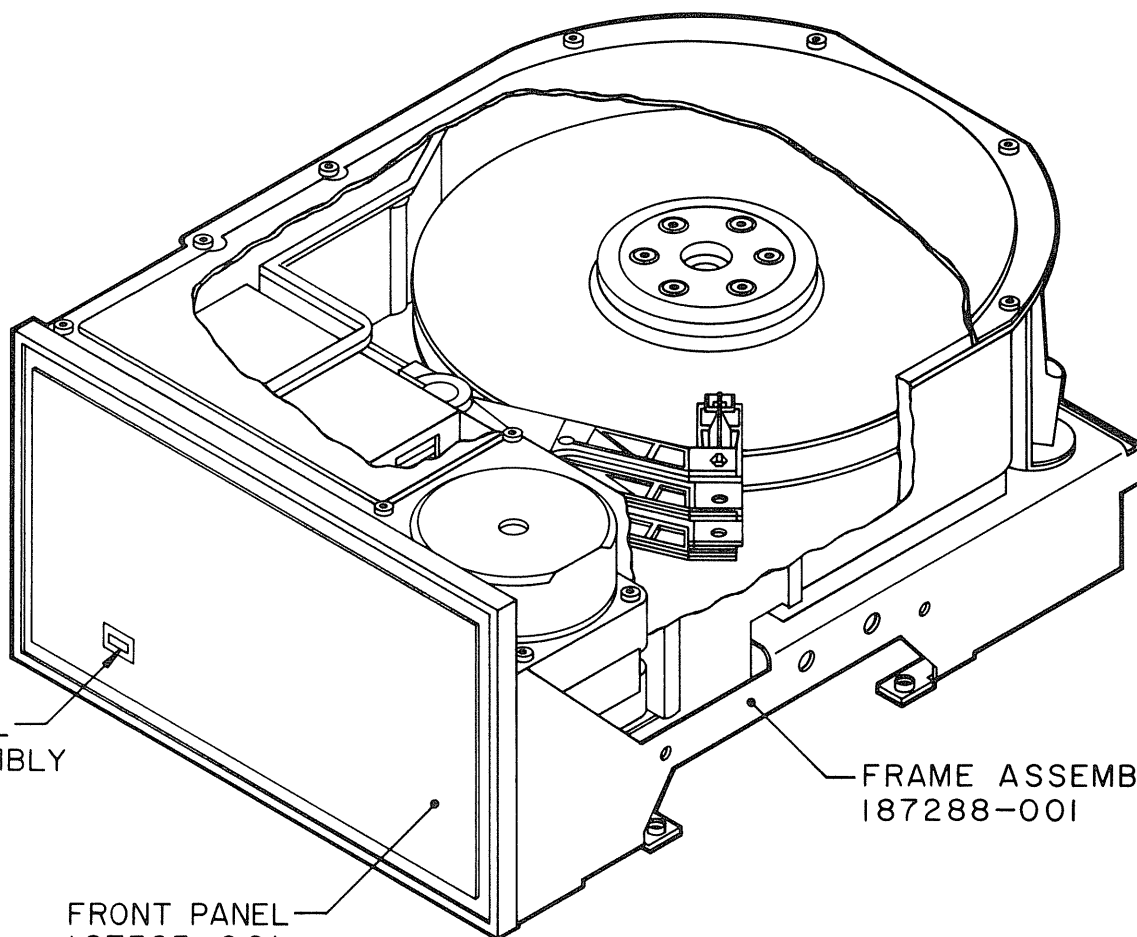
## **RECOMMENDED SPARE PARTS LIST AND MAJOR ASSEMBLIES**

All assemblies with part numbers are available for purchase as spare replacement parts. The items without alpha designators on the drawings are for reference only, and cannot be purchased as spare replacement parts.

If an assembly has been determined faulty, and is not listed as a spare replacement, the drive must be returned to the manufacturer for repair.

## RECOMMENDED SPARE PARTS LIST

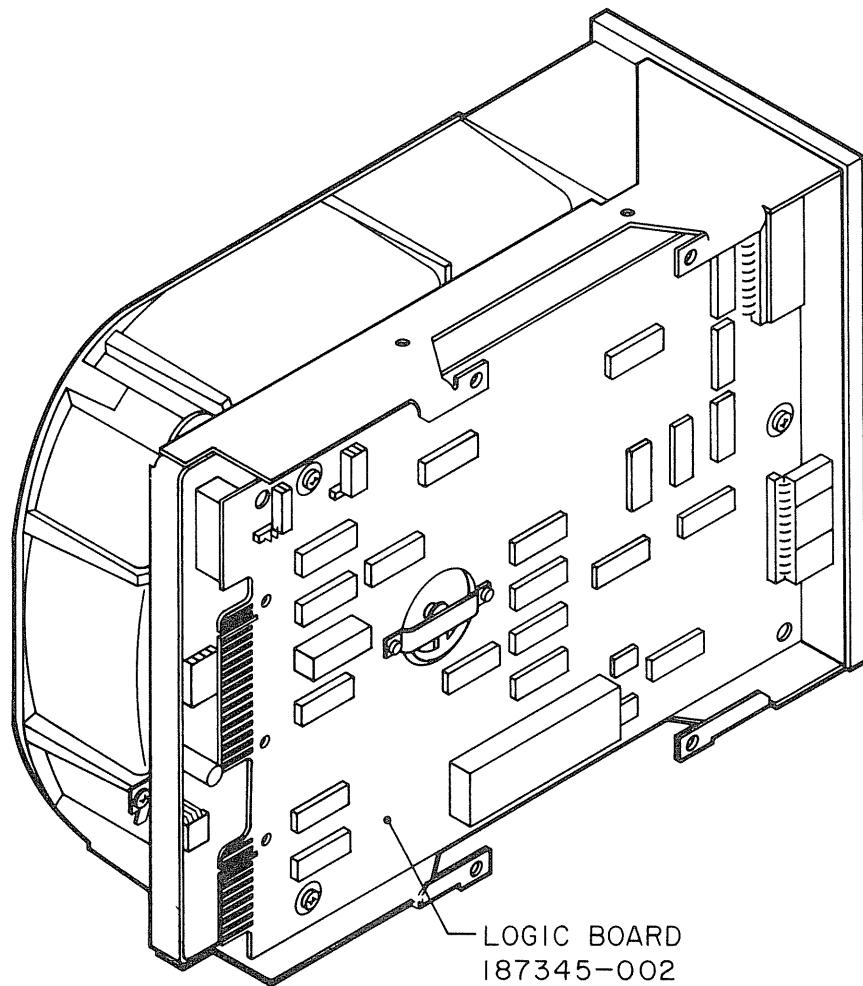
| Description                         | Part Number            |
|-------------------------------------|------------------------|
| Brake Assembly.....                 | 962010-001             |
| Index Assembly.....                 | 187010-001             |
| Track 0 Assembly.....               | 187174-001             |
| Frame Assembly.....                 | 187288-001             |
| Front Panel.....                    | 187323-001             |
| Spindle Control Board.....          | 187065-001             |
| Logic Board.....                    | 187345-002             |
| Front Panel L.E.D. Assembly.....    | 187018-001             |
| Single Pack Shipping Container..... | 187125-001 (Not Shown) |
| Four Pack Shipping Container.....   | 187125-002 (Not Shown) |

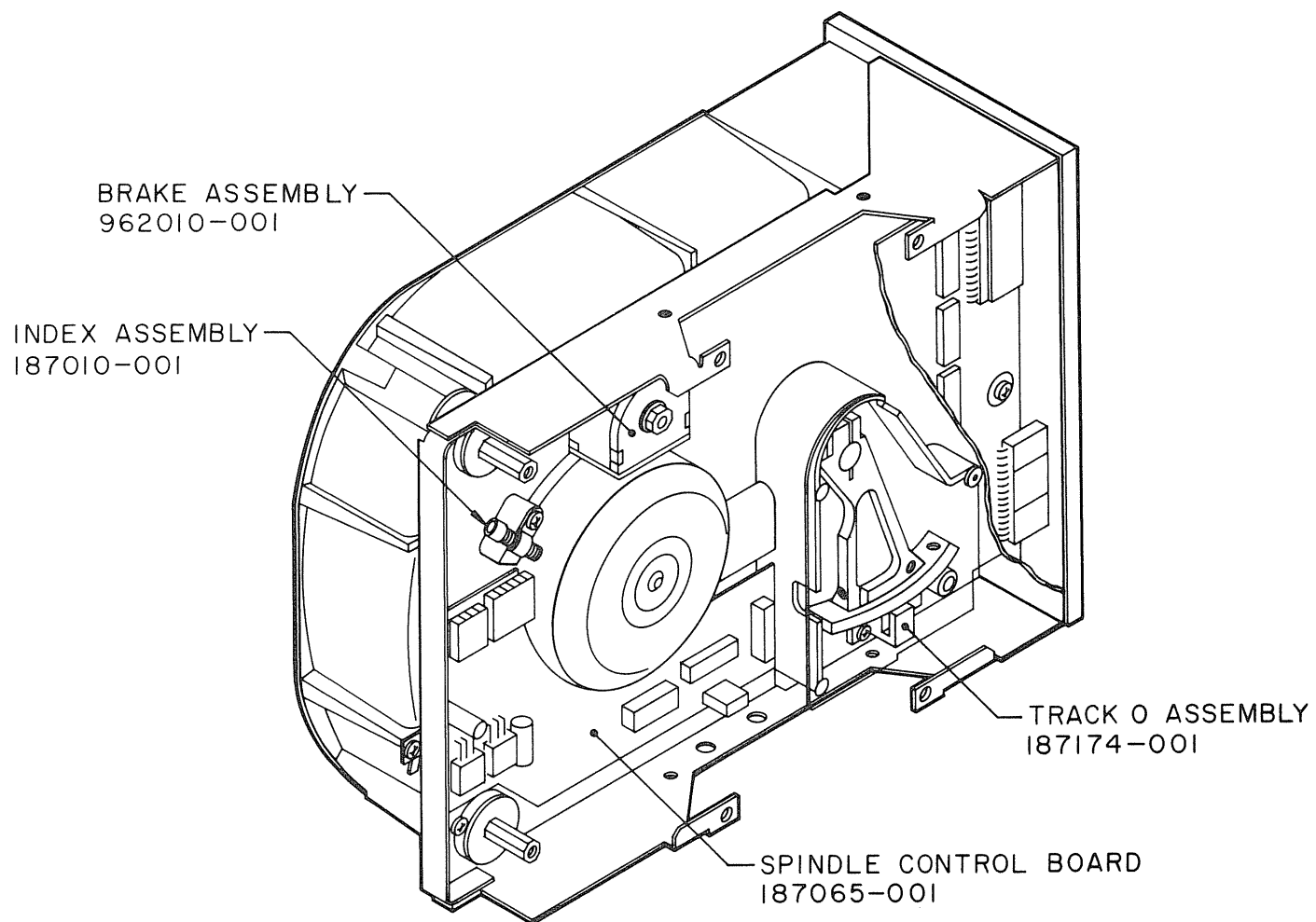


FRONT PANEL  
L.E.D. ASSEMBLY  
187018-001

FRONT PANEL  
187323-001

FRAME ASSEMBLY  
187288-001









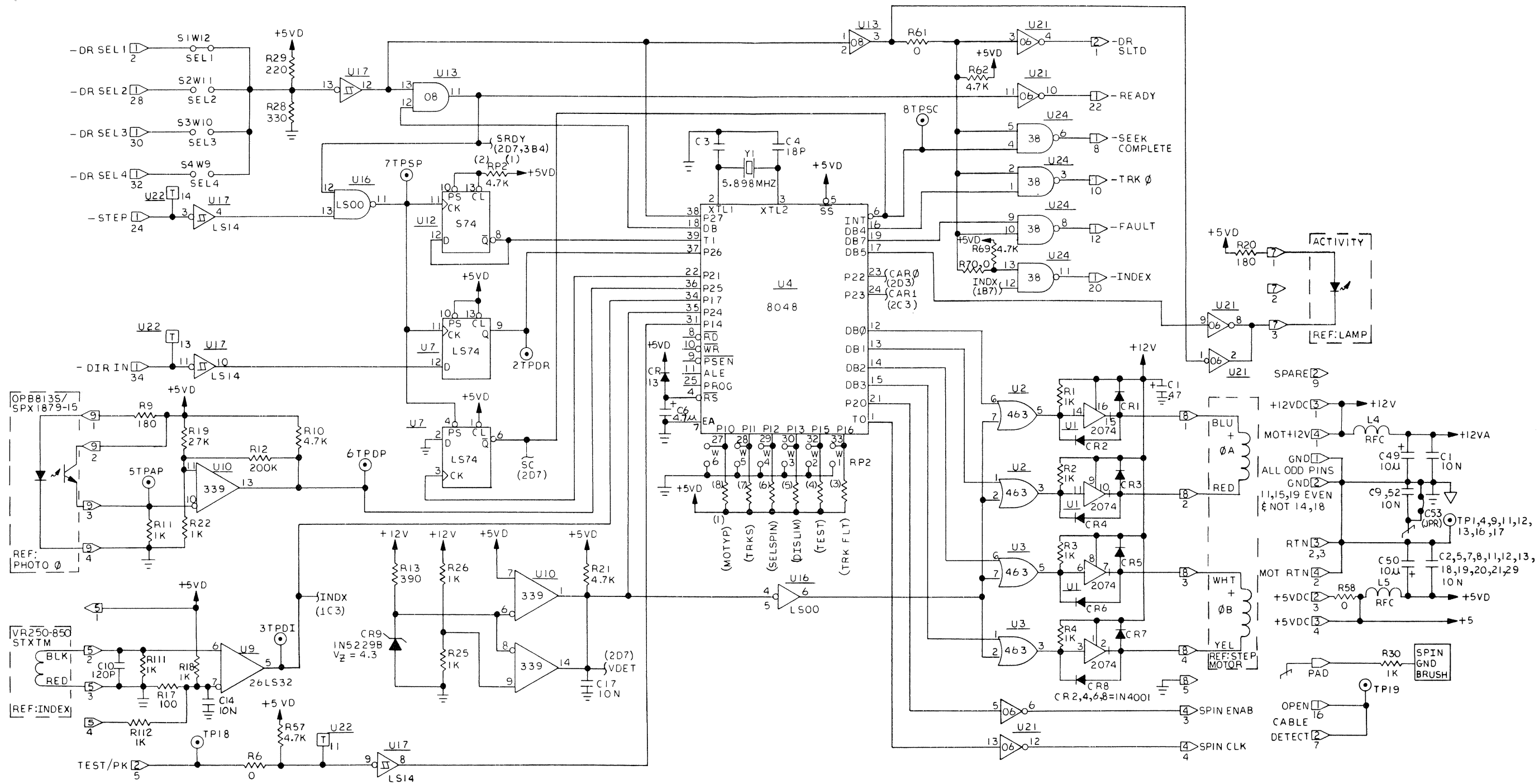
# APPENDIX B

## CIRCUIT BOARD SCHEMATICS AND ASSEMBLY DRAWINGS

This appendix contains the current circuit board schematics and assembly drawings for the TM500 series of disk drives.

| Drawing Number   | Title                                       | Page Number   |
|------------------|---|---------------|
| 187340-001 REV L | Control and Data<br>Circuit Board Schematic | B-2, B-3, B-4 |
| 187345-001 REV S | Control and Data<br>Circuit Board Assembly  | B-5           |
| 187065-001 REV J | Spindle Control<br>Circuit Board Assembly   | B-6           |
| 187060-001 REV E | Spindle Control<br>Circuit Board Assembly   | B-7           |





6. X DENOTES CONNECTOR JX, PIN Y.  
 5. INDUCTORS ARE IN  $\mu$ H, 10%.  
 4. DIODES ARE IN4446 OR EQUIV.  
 3. CAPS ARE IN PF, NF, OR  $\mu$ F, 25V MIN. TOLERANCE IS 10% FOR VALUES ABOVE 1N, 5% OTHERWISE.  
 2. 1% RESISTORS ARE 1/8W.  
 1. RESISTORS ARE IN OHMS, 1/4W 5%.

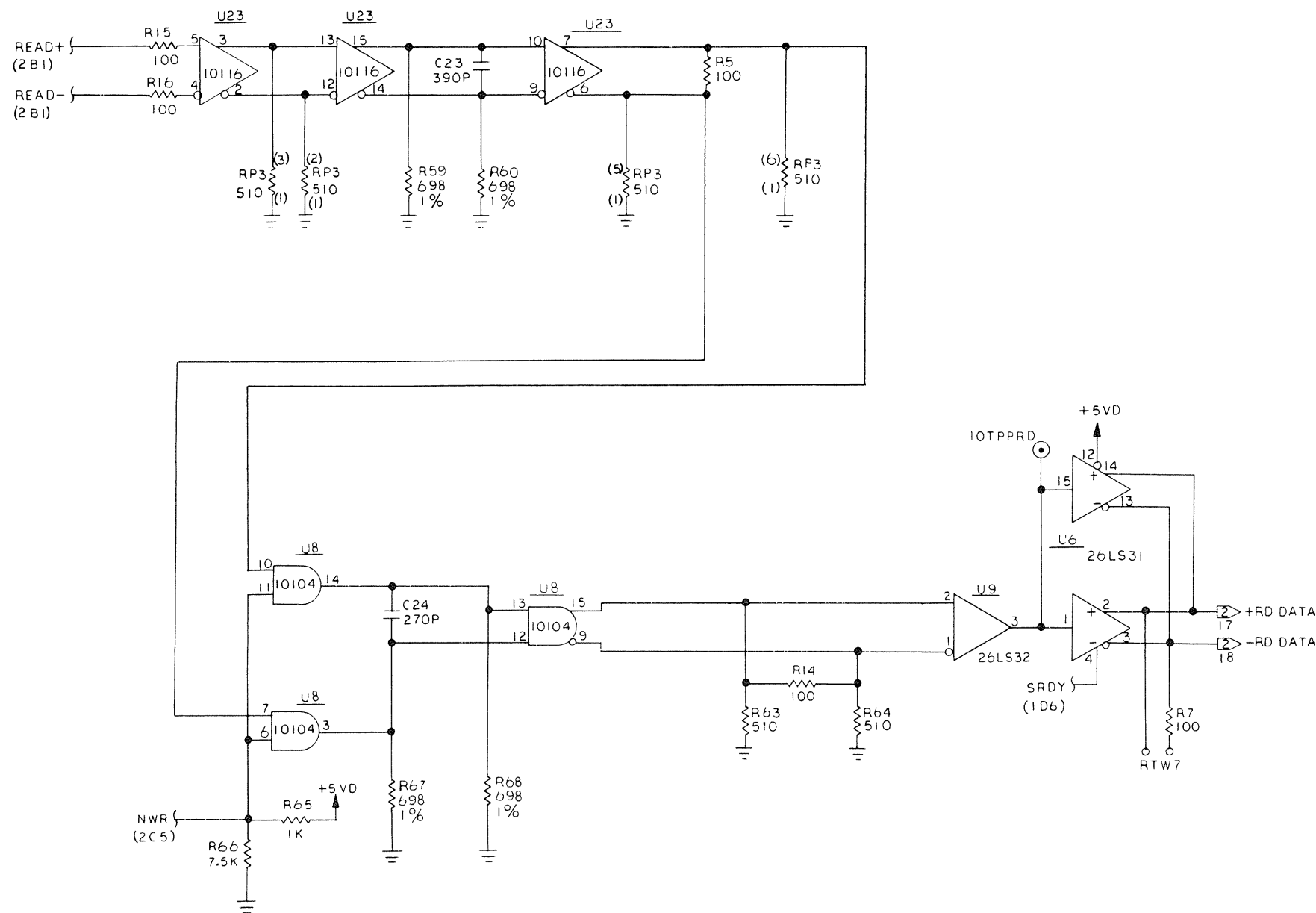
9. UNVALUED COMPONENTS ARE OMITTED.

8. T<sub>x</sub> DENOTES PIN X OF (U22) TERM PAK. =

7. DENOTES TEST POINT.

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 CONTROL AND DATA  
 CIRCUIT BOARD SCHEMATIC  
 187340-001 REV L  
 SHEET 1 OF 3



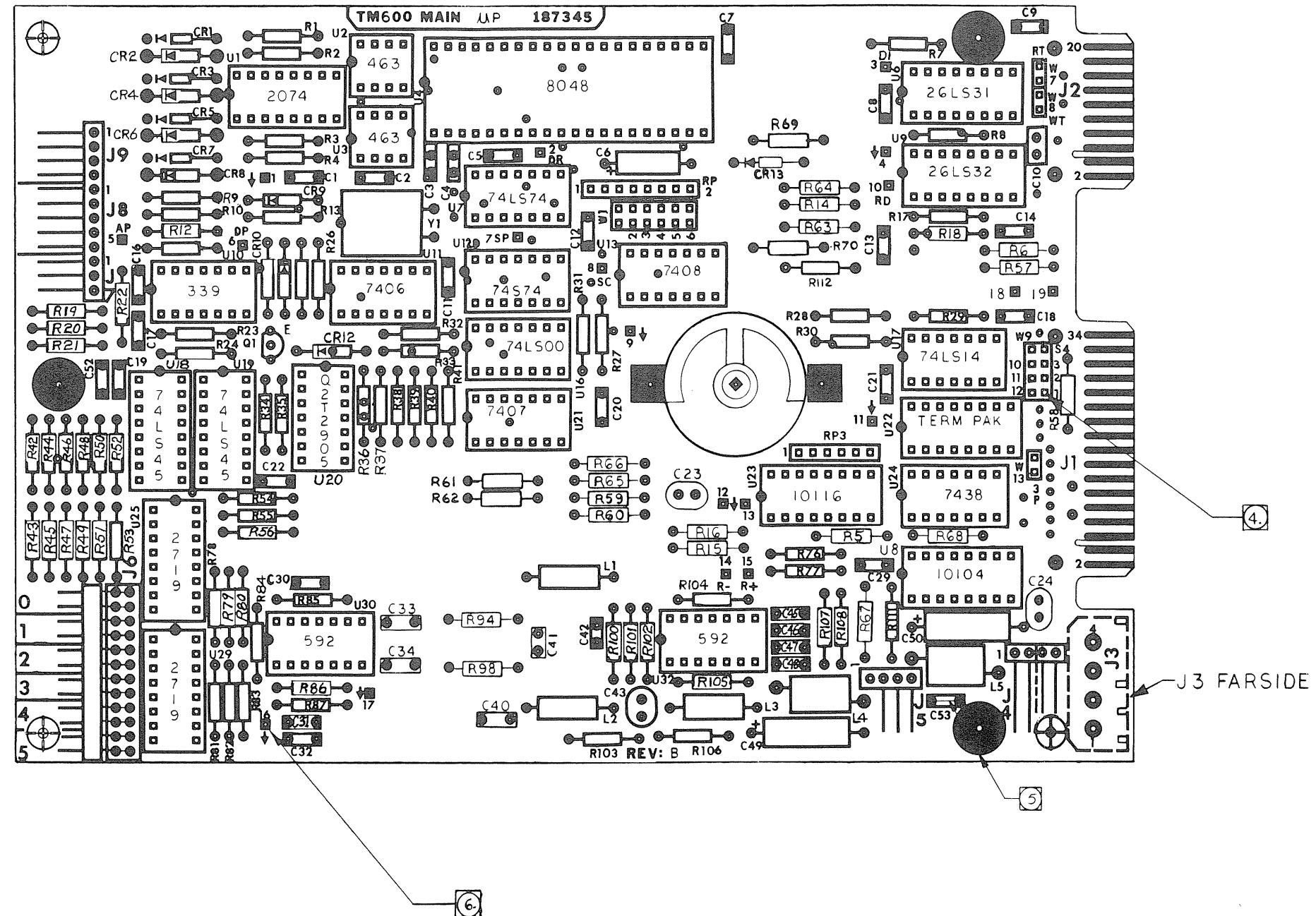


| I.C. LOCATION AND VOLTAGE CHART |          |       |       |           |        |
|---------------------------------|----------|-------|-------|-----------|--------|
| LOCATION                        | TYPE     | +5    | +12   | GND       | UNUSED |
| U16                             | 74LS00   | 14    |       | 7         |        |
| U21                             | 7406     | 14    |       | 7         |        |
| U11                             | 7407     | 14    |       | 7         |        |
| U13                             | 7408     | 14    |       | 7         |        |
| U17                             | 74LS14   | 14    |       | 7         | 1/6    |
| U24                             | 7438     | 14    |       | 7         |        |
| U18,U19                         | 7415     | 16    |       | 8         |        |
| U7                              | 74LS74   | 14    |       | 7         |        |
| U12                             | 74S74    | 14    |       | 7         |        |
|                                 |          |       |       |           |        |
| U10                             | 339      |       | 3     | 12        | 1/4    |
| U2,U3                           | 463      | 8     |       | 4         |        |
| U30,U32                         | 592      |       | NOTED | NOTED     |        |
| U1                              | UNL2074  |       | NOTED | 4,5,12,13 |        |
| U6                              | 26LS31   | 16    |       | 8         | 2/4    |
| U9                              | 26LS32   | 16    |       | 8         | 1/4    |
| U25,U29                         | 2719     |       |       |           |        |
| U20                             | Q2T2905  |       | NOTED |           | 1/4    |
|                                 |          |       |       |           |        |
|                                 |          |       |       |           |        |
| U4                              | 8048     | 26,40 |       | 20        |        |
| U8                              | 10104    | 1,16  |       | 8         | 1/4    |
| U23                             | 10116    | 1,16  |       | 8         |        |
|                                 |          |       |       |           |        |
|                                 |          |       |       |           |        |
| U14                             |          |       |       |           |        |
| U22                             | TERM PAK | 16    |       | 7,8       | 4/12   |

| SHUNT PLUG PROGRAMMING                                      |                  |                    |  |
|---|------------------|--------------------|--|
| W1-W13 SHUNT  | FUNCTION         | FACTORY PROGRAMMED | USAGE                                    |
| W1  | TRK FAULT        | 0                  | INSTALL FOR EXCESS TRK FAULT             |
| W2  | TEST             | 0                  | INSTALL FOR FACTORY TEST                 |
| W3  | DISABLE LIMIT    | 0                  | INSTALL TO DISABLE SOFT LIMITS           |
| W4  | SPIN SELECT      | 0                  | INSTALL FOR SPIN SELECT                  |
| W5  | TRACKS           | S                  | INSTALL FOR S VERSION ONLY               |
| W6  | MOTOR TYPE       | -                  | INSTALL FOR TYPE 18 MOTOR                |
| RTW7  | READ TERMINATOR  | I                  | CLOSE ONLY@END DRIVE OF DAISY CHAIN DATA |
| WTW8  | WRITE TERMINATOR | I                  | CLOSED FOR RADIAL DATA                   |
| S4W9  | DRIVE SELECT 4   | 0                  | INSTALL 1 OF 4 PLUGS ONLY                |
| S3W10   | DRIVE SELECT 3   | 0                  | PLUG CORRESPONDS TO                      |
| S2W11   | DRIVE SELECT 2   | 0                  | DRIVE ADDRESS                            |
| S1W12   | DRIVE SELECT 1   | I                  |  |
| 3PW13   | 3 DISC           | 603                | CLOSE FOR MODEL 603 ONLY                 |
| U22   | TERMINATOR PAK   | I                  | INSTALL IN END DRIVE OF DAISY CHAIN      |
| O = OMIT                      S = CLOSE FOR S VERSION ONLY  |                  |                    |  |
| I = INSTALL                      603 = CLOSE FOR TM603 ONLY |                  |                    |  |

| REFERENCE DESIGNATORS |                                       |
|-----------------------|---------------------------------------|
| LAST USED             | UNUSED                                |
| C54                   | C15,25-28,35-39,44,51                 |
| CR13                  |                                       |
| J9                    |                                       |
| L5                    |                                       |
| Q1                    |                                       |
| R111                  | R71 THRU 75,84,88-93,95-97,99,109,110 |
| RP3                   | RPI                                   |
| TP19                  |                                       |
| U32                   | U5,14,15,26-28,31                     |
| W13                   |                                       |
| Y1                    |                                       |

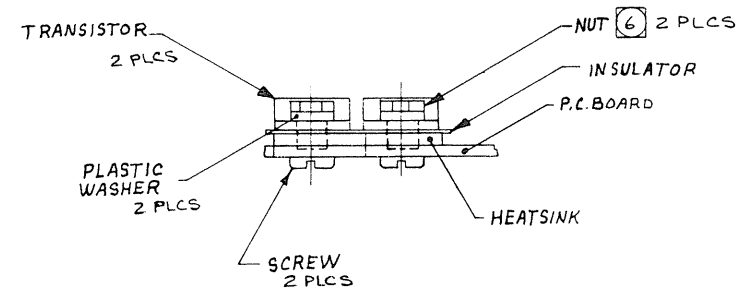
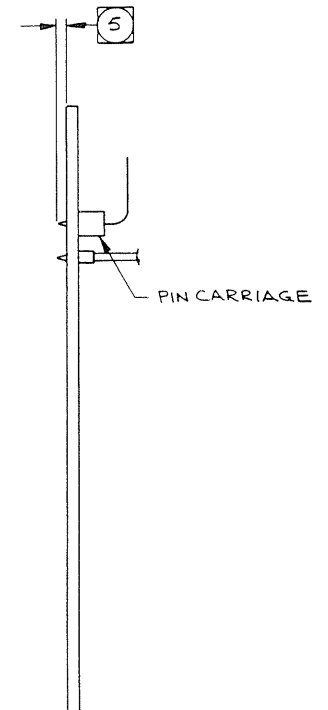
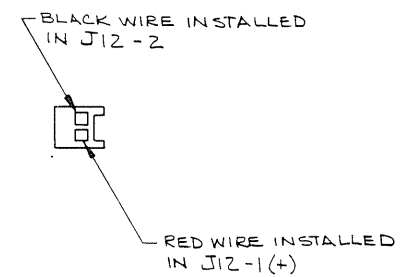
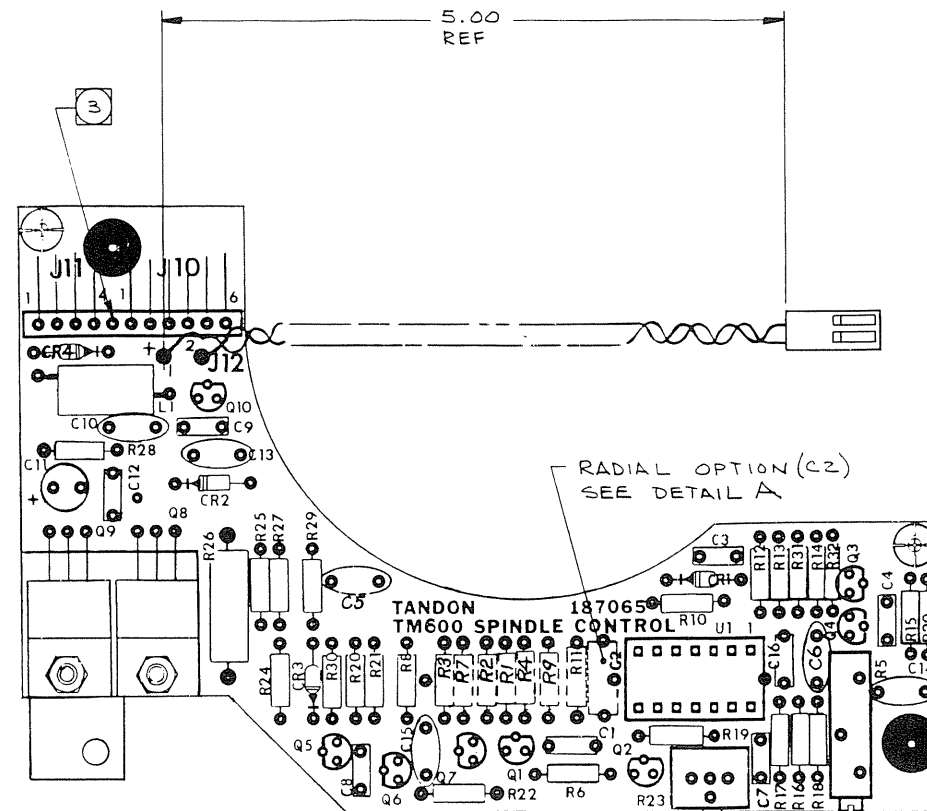
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CIRCUIT BOARD SCHEMATIC  
187340-001 REV L  
SHEET 3 OF 3



- (6) T.P. WIRE TO EXTEND .25 MIN, .30 MAX ABOVE COMPONENT SIDE OF P.C. BOARD IN PLACES.
- (5) SOLDER #24AWG UNINSULATED TIN COATED SOLID COPPER WIRE MAY SUBSTITUTE 1/4W RESISTOR LEAD WIRE.
- (4) .025 SQ. SHUNT JUMPER POSTS (W1-W13) TO BE INSTALLED  $\perp$  TO BOARD SURFACE WITH FLATS TO BE  $\parallel$  TO BOARD EDGES, 26 PLACES.
3. SOLDER #24 AWG UNINSULATED TIN COATED SOLID COPPER WIRE MAY SUBSTITUTE 1/4 W RESISTOR LEAD WIRE.
2. REF. DOCUMENT: 187340-001-CIRCUIT SCHEMATIC  
187341-001 ARTWORK
1. SEE SHEET 1 THRU 3 FOR PART LIST.

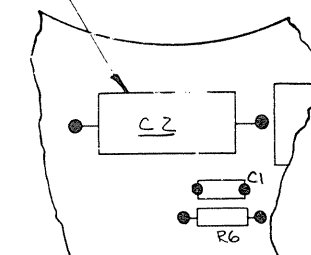
| REFERENCE DESIGNATORS |                                    |
|-----------------------|------------------------------------|
| LAST USED             | UNUSED                             |
| C53                   | C15,25-28,35-39,44,51              |
| CR13                  |                                    |
| J9                    |                                    |
| L5                    |                                    |
| Q1                    |                                    |
| R111                  | R71 THRU 75,88-93,95-97,99,109,110 |
| RP3                   | RPI                                |
| TP19                  |                                    |
| U32                   | U5,14,15,26-28,31                  |
| W13                   |                                    |
| Y1                    |                                    |

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CIRCUIT BOARD ASSEMBLY  
187345-001 REV S  
SHEET 1 OF 1



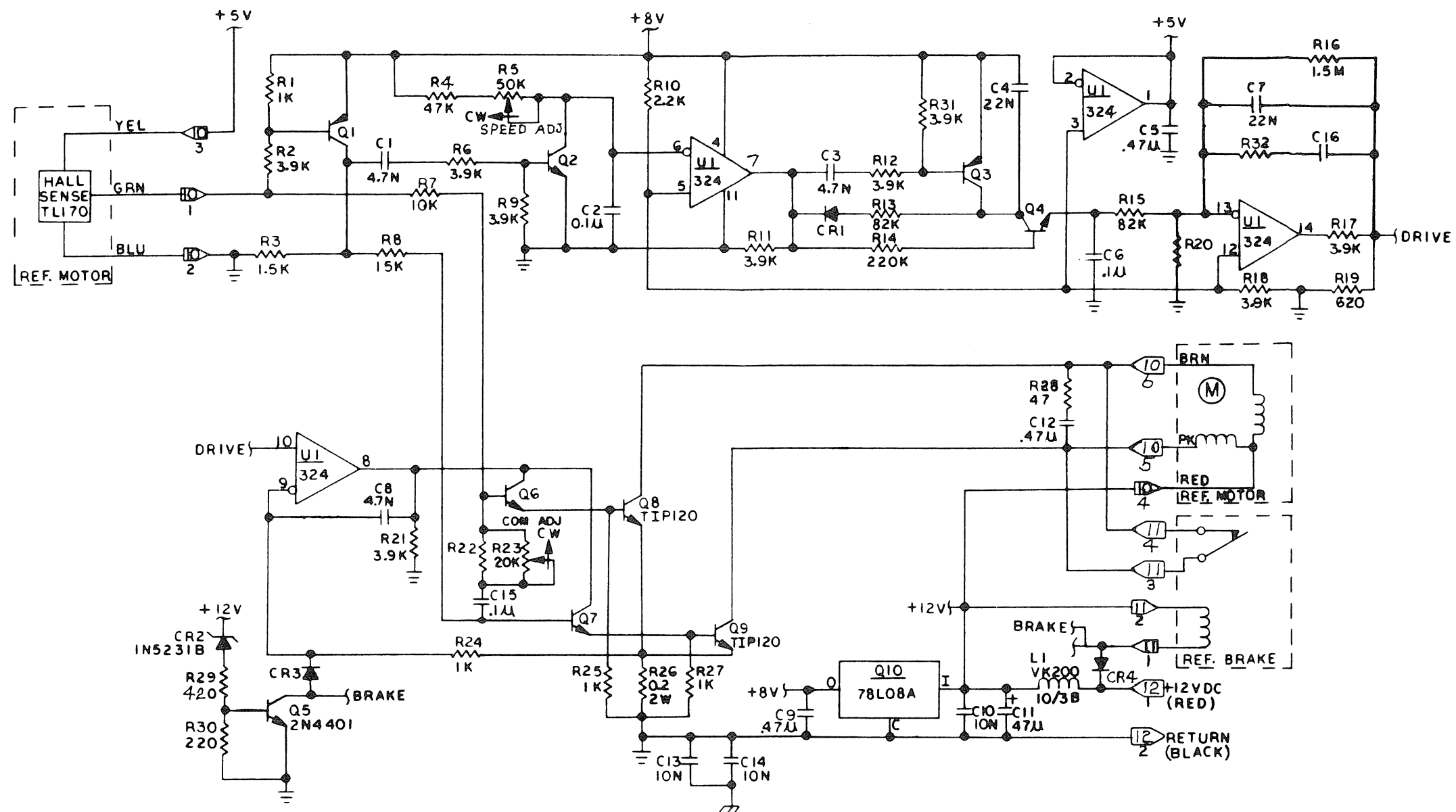
7. REF DOCUMENT: 187060-001 CIRCUIT SCHEMATIC  
187061-001 ARTWORK
6. TORQUE 4 5 INCH LBS NOMINAL.
5. MAX LENGTH OF COMPONT LEADS BELOW SOLDER SIDE BOARD  
AFTER ASSEMBLY & TRIMMING SHALL NOT EXCEED .08 INCH
4. COMPONENT HEIGHT EXCEPT C11, SHALL NOT EXCEED  
.45 INCH ABOVE BOARD. CAPACITOR C11, NOT TO EXCEED  
.50 INCH ABOVE BOARD.
3. CUT PIN TANGENT TO PIN CARRIAGE.
2. THIS ASSEMBLY SHALL BE MADE FROM P.C.B. DETAIL 187060-001.
1. ASSEMBLE PER STANDARD MANUFACTURING METHODS.

AXIAL OPTION



DETAIL A

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187065-001 REV J  
SHEET 1 OF 1



7. UNVALUED COMPONENTS ARE OMITTED.



6. Y OR Y DENOTES CONNECTOR X, PIN Y.

5. PNP TRANSISTORS ARE 2N4125.

4. NPN TRANSISTORS ARE 2N4124.

3. DIODES ARE 1N4446 OR EQUIV.

2. CAPACITORS ARE 25V, 10%, PF, NF, OR UF.

1. RESISTORS ARE IN OHMS, 1/4 W, 5%.

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187060-001 REV E  
SHEET 1 OF 1





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